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Real-Time Fluorescence Lifetime Actuation for Cell Sorting using a CMOS SPAD Silicon Photomultiplier

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Time-correlated single photon counting (TCSPC) is a fundamental fluorescence lifetime measurement technique offering high signal to noise ratio (SNR). However, its requirement for complex software algorithms for histogram processing restricts throughput in flow cytometers and prevents on-the-fly sorting of cells. We present a single-point digital Silicon Photomultiplier (SiPM) detector accomplishing real-time fluorescence lifetime-activated actuation targeting cell sorting applications in flow cytometry. The sensor also achieves burst-integrated fluorescence lifetime (BIFL) detection by TCSPC. The SiPM is a single-chip complementary metal oxide semiconductor (CMOS) sensor employing a 32×32 single-photon avalanche diode (SPAD) array and 8 pairs of time-interleaved time to digital converters (TI-TDCs) with a 50 ps minimum timing resolution. The sensor’s pile-up resistant embedded centre of mass method (CMM) processor accomplishes low-latency measurement and thresholding of fluorescence lifetime. A digital control signal is generated with a 16.6 μs latency for cell sorter actuation allowing a maximum cell throughput of 60,000 cells per second and an error rate of 0.6%. © 2015 Optical Society of America

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Measurement of fluorescence lifetime is a well-established experimental technique to probe protein-protein interactions occurring in live cells [1]. In flow cytometry it can be used as a parameter to isolate sub-populations of cells from a heterogeneous sample by a method called Fluorescence Activated Cell Sorting (FACS) [2]. To provide the ability to measure the fluorescence lifetime of fluorophores to nanosecond precision, various time-resolved methods have been proposed [3]. Amongst these techniques Time-Correlated Single Photon Counting (TCSPC) provides a method to time single photon arrivals with high resolution and efficiency [4].

The limiting factor of sorting techniques in flow cytometry is the complexity and the large volume of fluorescence data to be processed to detect the passage of each individual cell and identify its chemical or physical characteristics to perform a reliable sorting decision. For this reason, existing time-resolved fluorescence lifetime flow cytometers such as reported in [5] and [6] do not perform cell sorting. Processing of the fluorescence signal is performed offline on a PC causing large delays from slow data transfer rates. In addition, phase-filtered sorting cytometers such as presented in [7] report low lifetime resolution preventing cell populations with similar lifetimes to be distinguished.

Recent advances in Complementary Metal Oxide Semiconductor (CMOS) sensors have permitted the integration of complete Silicon Photomultiplier (SiPM) detectors with high-resolution timing directly on-chip [8] as well as fast communication with embedded systems such as field programmable gate arrays (FPGAs) offering greatly reduced transfer rates and eliminating the requirement of a PC for data processing. We present a SPAD-based SiPM detector, which to the best of our knowledge is the first sensor able to perform low-latency time-resolved fluorescence lifetime measurement and online sorting decisions to drive an actuation system for fluorescence lifetime cytometric sorting. The SiPM is implemented on a single-chip 1.3 mm by 1.7 mm in size and includes a 32×32 SPAD array with 10% fill factor [8]. The sensor encompasses two modalities: (1) a time continuous burst-integrated fluorescence lifetime (BIFL) TCSPC histogramming system targeting cytometric studies and sorting calibration and (2) a real-time sorting actuation system based on FPGA embedded Centre of Mass Method (CMM) lifetime computation and thresholding.

In TCSPC photon arrival times are converted into a representation of the captured fluorescence intensity decay curve. Short laser pulses give rise to fluorescence emission in the sample. The time delay between the fluorescence detection with respect to the excitation pulse that triggered the fluorescence is measured and converted into a digital signal which is then used to build a
histogram of photon counts as a function of photon arrival time. Each histogram is fitted with an exponential model to estimate the fluorescence lifetime of the sample [3].

The TCSPC system on the SiPM is composed of a pair of time-interleaved to digital converters (TI-TDCs) performing high-resolution timing. TI-TDCs output the photon arrival times in the form of 10-bit time stamps during an exposure to a parallel bus, which directly links to the FPGA. Histograms representing the fluorescence decay are accumulated for a user-preset exposure time by incrementing counters in the FPGA memory addressed by the photon time stamps. During the experiment, the contents of the memory are continuously transferred to a PC to calculate the fluorescence intensity and fluorescence lifetime for each exposure. A diagram of the TCSPC system on the SiPM is shown in Fig. 1.

On the FPGA, two time-interleaved 256-byte histogramming memories are composed of 256 addresses with an 8-bit counter per address allowing collection of photons in 256 time-bins for each exposure. The time resolution of the histogram bins in the memory is set by the TDC resolution and is user-configurable to be between 50 ps and 3.2 ns giving a timing dynamic range of each histogram from a minimum 12.8 ns up to 0.82 µs. Two time-multiplexed histogramming memories and first-in, first-out (FIFO) buffers are implemented to enable continuous histogram accumulation in one memory while the content of the other is being transferred to the PC. The FIFO memories are 16 KB each allowing storage of up to 512 histograms per FIFO before transfer to PC. The read-out rate of the USB is 266 kB/s enabling continuous detection with no time-gaps in photon capture between successive exposures with a minimum 1 ms exposure time. The histogram is processed in software by an adapted version of the DecayFit exponential fitting algorithm [9, 10] in MATLAB to compute the fluorescence lifetime recorded for each exposure.

The Centre of Mass Method (CMM) is an averaging algorithm for fluorescence lifetime approximation [11]. It is embedded in the SiPM hardware, allowing low-latency fluorescence lifetime estimation, thresholding and direct sorting control without the requirement for transfer to a host PC [8, 11]. The method calculates fluorescence lifetime by averaging the photon arrival times measured by the on-chip TDCs. The arrival times $N_i$ within each time-bin $i$ are summed and divided by the total number of photon events $N_c$ detected in an exposure to obtain a first-order approximation of the lifetime $\tau$ as shown in Eq. 1 [11].

$$\tau = \frac{\sum_{i=0}^{M} i \cdot N_i}{N_c}$$  \hspace{1cm} (1)

The low-latency lifetime computation and thresholding system is partitioned between hardware on-chip for photon event accumulation and firmware for CMM division and generation of the sorting actuation on FPGA as shown in the system diagram in Fig. 2.

A ripple counter counts the number of photon events that fall within a user-set time window storing the result in a 28-bit accumulator. The time stamps of the detected photon events output from the 8 pairs of CMM-dedicated TI-TDCs are summed by an adder tree and stored into a 36-bit accumulator [8].

The pre-calculated arrival time sum and total photon count collected in an exposure are concatenated into a 64-bit number, which is streamed at 12 Mb/s from an on-chip serial interface (SI) directly to the FPGA with a transfer delay of only 5.3 µs per exposure. The total number of photons detected in each exposure is monitored on the FPGA and a thresholding function interrupts data transfer and processing for exposures with photon counts recorded below the background noise level. This allows the system to signal the absence of a specimen through the cytometer focal volume.

To obtain the fluorescence lifetime from the CMM data received from the chip, a divider is implemented on FPGA which performs the division in Eq. 1. The division operation is based on a long-division firmware algorithm as proposed in [12], which performs iterative subtractions of the total photon count from the time-code sum checking the sign of the difference at each subtraction. Each subtraction iteration is completed within 2 FPGA clock cycles amounting to a total worst-case division and thresholding delay of 11.3 µs when operating at 48 MHz FPGA system clock rate. This time combined with the 5.3 µs SI delay means that the lifetime result can be available to the sorter after only 16.6 µs from the end of the exposure. Since the lifetime calculation is done on FPGA there is only a 5.3 µs delay between the end of one exposure and the next allowing the SiPM to start a new exposure without being affected by processing delays.

The FPGA generates an actuation signal for downstream cell sorting hardware by comparing the fluorescence lifetime result obtained at the end of the iterative subtractions against a user-set programmable lifetime threshold. If the lifetime result is below the sorting threshold a binary 1 is output at the pins of the FPGA, while if the lifetime is found to be above threshold a binary 2 is output. Binary 3 is output when no lifetime is recorded due to the photon counts being below the noise-level count threshold.

The SiPM was tested and characterised in laser-scanning ex-
periments using samples of two different fluorophore-tagged microparticles. Melamine FITC-stained microparticles in a 1.91×10⁷ particle/ml suspension (Sigma-Aldrich, Irvine, UK) are 10 µm in diameter and feature a fluorescence lifetime of 4.1 ± 0.1 ns [13]. Latex Firefli-stained microparticles in a 2.78×10⁹ particles/ml suspension (Thermo Scientific, Waltham, MA) are 1.9 µm in diameter with a fluorescence lifetime of 2.0 ± 0.2 ns [5]. 0.1 mm-inner diameter capillaries (VitroCom, Mountain Lakes, NJ) containing undiluted suspensions of the fluorescent microparticles were positioned close and parallel to each other on a microscope slide as shown in Fig. 3. Capillaries were sealed at both ends with epoxy resin to prevent solvent evaporation.

A diagram of the experimental setup is shown in Fig. 4. The excitation source is a 473 nm, 20 MHz pulsed laser BDL-473-SMC (Becker & Hickl GmbH, Berlin, Germany) with an average power of 0.2 mW. A scanning microscope stage is controlled through the SPC-830 TCSPC Imaging Module (Becker & Hickl GmbH, Berlin, Germany). The laser beam was scanned across the microscope slide and the detector captured the fluorescence light for 1000 exposures of 10 ms each. The photon bursts detected were processed and the fluorescence lifetime for each exposure was plotted as a function of time as shown in Fig. 5. The histogram showing the distribution of fluorescence lifetimes detected is shown in Fig. 6.

The plot in Fig. 5 shows the correct identification of each different microparticle population within the capillaries as the microscope scanned across the slide. Analysis of the histogram in Fig. 6 shows two Gaussian distributions representing the detection of the two fluorophore-tagged populations of microparticles. The mean of the populations differ from the quoted lifetimes of the fluorophore by a worst-case 0.2 ns. It was found that the error on the mean and the standard deviation could be further reduced by increasing the detected photon count rate and minimizing the noise floor by disabling the SPADs with high dark count rate (DCR) while focusing the fluorescence from the illuminated microparticles on the enabled SPADs. To this effect, a reduction of the average DCR per SPAD from 10 kHz to 100 Hz by enabling lower-DCR SPADs, reported an increase in the SNR from 3 to 300. With higher count rates, lower exposures to improve BIFL time resolution can also be accomplished by a more efficient use of the USB peripheral data-rate which can improve the TCSPC maximum exposure rate increasing it above 100 kHz.

The same experiment was repeated using the CMM mode of the chip to test the sorting actuation function of the sensor. Actuation bits from the FPGA output pins were recorded and mapped as a function of time for 4000 exposures of 10 ms each as shown in Fig. 7. The experiment was repeated for different exposures and the data analysed to calculate the error rate of the SiPM in outputting the correct actuation bits for the corresponding microparticle type. The error rates measured for the tested exposure times are shown in Table 1. Error rates were calculated by direct comparison of the actuation signal with reference lifetime values obtained from TCSPC histogram measurements of the same sample. The plot in Fig. 7 shows the ability of the CMM actuation to correctly identify the two populations of microparticles on the slide. The alternating pattern of fluorophore-tagged microparticles visible in the intensity signal plot in Fig. 7(a) was replicated in the actuation bit pattern in (b). In this experiment the lifetime actuation threshold was set at 3 ns, midway between the values of the lifetimes quoted for the two fluorophore types allowing...
the SiPM to differentiate detections between lifetimes below threshold for Firefli-tagged microparticles and above threshold for FITC microparticles. A preliminary TCSPC experiment allowed the measurement of the noise level to set the count threshold below which the CMM actuation signalled the absence of specimen detection (N/A in the plot in Fig. 7).

While the microparticle scanning experiment demonstrated real-time calculation of fluorescence lifetime, thresholding and sorting using the SiPM, the experimental setup cannot completely reproduce a flow cytometry environment with live cells. Due to the clustering of the microparticles in the capillaries, transient peaks of fluorescence originating from multiple microparticles within single exposures yielded a higher count rate than that obtainable from individual cell transitions in flow, thus prompting for solutions to increase the detected SNR.

The maximum sorting rate limit of 60,000 cells per second is set by the CMM actuation delay of 16.6 µs. This figure does not however account for the exposure time necessary to collect enough photons to perform a sorting decision, which is determined by the entire cytometric system including the optics and fluids as well as the electronics. The minimum exposure time of 2 ms used in the experiment limits the sorting rate to only 500 particles per second. The exposure was set to a high value to obtain a SNR high enough to yield a low error rate in the CMM calculation of lifetime. However, as the noise-level is fixed and is primarily set by the DCR of the SPADs, the detected photon count rate can be increased by selecting lower-DCR SPADs [14] and an optimised optical system employing a microscope to focus the fluorescence signal only over the enabled SPAD area of the SiPM. In addition, the use of a higher frequency pulsed laser also increases the photon count rate allowing the use of smaller exposures in the order of microseconds to better exploit the low-latency CMM embedded lifetime actuation reaching sorting rates greater than tens of thousands of cells per second.

We presented a CMOS 32×32 SPAD array SiPM accomplishing low-latency fluorescence lifetime actuation by CMM and burst detection by TCSPC. Employing 8 pairs of TI-TDCs and splitting the lifetime calculation between a precalculation on-chip and division and thresholding on FPGA, the SiPM is capable of measuring fluorescence lifetime within 16.6 µs with error-rates below 0.6%. The detector’s fast embedded calculation of fluorescence lifetime makes it ideal to target novel sorting applications in flow cytometry by an increased throughput, high time resolution and on-the-fly sorting actuation.

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