



THE UNIVERSITY *of* EDINBURGH

Edinburgh Research Explorer

On Protection by Layout Randomization

Citation for published version:

Abadi, M & Plotkin, GD 2012, 'On Protection by Layout Randomization', *ACM Transactions on Information and System Security*, vol. 15, no. 2, 8. <https://doi.org/10.1145/2240276.2240279>

Digital Object Identifier (DOI):

[10.1145/2240276.2240279](https://doi.org/10.1145/2240276.2240279)

Link:

[Link to publication record in Edinburgh Research Explorer](#)

Document Version:

Peer reviewed version

Published In:

ACM Transactions on Information and System Security

General rights

Copyright for the publications made accessible via the Edinburgh Research Explorer is retained by the author(s) and / or other copyright owners and it is a condition of accessing these publications that users recognise and abide by the legal requirements associated with these rights.

Take down policy

The University of Edinburgh has made every reasonable effort to ensure that Edinburgh Research Explorer content complies with UK legislation. If you believe that the public display of this file breaches copyright please contact openaccess@ed.ac.uk providing details, and we will remove access to the work immediately and investigate your claim.



On Protection by Layout Randomization

MARTÍN ABADI

Microsoft Research, Silicon Valley,
University of California, Santa Cruz, and
Collège de France
and

GORDON D. PLOTKIN

Microsoft Research, Silicon Valley and
LFCS, Informatics, University of Edinburgh

Layout randomization is a powerful, popular technique for software protection. We present it and study it in programming-language terms. More specifically, we consider layout randomization as part of an implementation for a high-level programming language; the implementation translates this language to a lower-level language in which memory addresses are numbers. We analyze this implementation, by relating low-level attacks against the implementation to contexts in the high-level programming language, and by establishing full abstraction results.

Categories and Subject Descriptors: D.4.6 [**Operating Systems**]: Security and Protection; D.3.4 [**Programming Languages**]: Processors

General Terms: Languages, Security, Theory

Additional Key Words and Phrases: Randomization, Protection

1. INTRODUCTION

Several techniques for protection are based on randomization (e.g., [Druschel and Peterson 1992; Yarvin et al. 1993; Kc et al. 2003; Forrest et al. 1997; Bhatkar et al. 2005; Bhatkar et al. 2003; Barrantes et al. 2005; Berger and Zorn 2006; Novark et al. 2008; Erlingsson 2007; Novark and Berger 2010]). The randomization may concern the layout of data and code within an address space, data representations, or the underlying instruction set. In all cases, the randomization introduces artificial diversity that can serve for impeding attacks. In particular, layout randomization can thwart attacks that rely on knowledge of the location of particular data and functions (such as system libraries). In addition, randomization can obfuscate program logic, against reverse engineering.

Authors' address: Microsoft Research Silicon Valley, 1288 Pear Avenue Mountain View, CA, 94043.

A preliminary version of this work was presented at the 23rd IEEE Computer Security Foundations Symposium (July 2010).

Permission to make digital/hard copy of all or part of this material without fee for personal or classroom use provided that the copies are not made or distributed for profit or commercial advantage, the ACM copyright/server notice, the title of the publication, and its date appear, and notice is given that copying is by permission of the ACM, Inc. To copy otherwise, to republish, to post on servers, or to redistribute to lists requires prior specific permission and/or a fee.

© 20YY ACM 0000-0000/20YY/0000-0001 \$5.00

Other techniques for protection address similar goals. For example, methods that ensure the integrity of control flow and data flow, statically or dynamically, can also regulate the use of system libraries (e.g., [Morrisett et al. 1999; Kiriansky et al. 2002; Abadi et al. 2009; Erlingsson 2007]). The static methods may be based on types or other static analyses. The dynamic methods often rely on reference monitors, whether implemented in hardware or software, at the boundaries of address spaces or inline. In addition to the diversity of their mechanisms, protection techniques vary in their goals and the underlying attack models. Some aim to offer precise, general guarantees, while others stop only some specific attack that can be easily modified to overcome the protection. They also vary in the difficulty of deploying them and in their costs. No single protection technique appears to be always superior to all others. In this paper we focus on layout randomization because it is in widespread use [PaX Project 2004; Howard and Thomlinson 2007], it has been subject to practical attacks (e.g., [Anonymous 2002; Shacham et al. 2004; Sotirov and Dowd 2008]), and it has hardly been studied rigorously.

We present layout randomization as part of an implementation for a high-level programming language. The language that we consider includes higher-order functions and mutable variables that hold natural numbers, which we call locations. Some of the locations are designated as public while others are designated as private, with the intent that an attacker should not have direct access to the latter. [For instance, consider a program that manipulates data that should remain secret or be protected from tampering, such as a value that indicates the authentication status of a communication channel \(as in \[Chen et al. 2005, Section 4.4\]\). The program may store this data in a private location; it may publish a function that internally uses the private location, and this function may be invoked by untrusted pieces of code. The implementation translates the high-level language to a lower-level language in which memory addresses are natural numbers; layout randomization consists in mapping the private locations to random addresses in data memory. If the data memory is large enough and the randomization good enough, then even an attacker with access to all of data memory \(for example, able to modify the contents of any particular memory address via a buffer overflow\) cannot find the private locations efficiently with high probability. \(Otherwise, the attacker may succeed, as demonstrated in actual exploits, e.g., \[Shacham et al. 2004\].\) We derive that the security properties that hold against attackers that cannot access the private locations directly continue to hold in this implementation, in a probabilistic sense and against resource-bounded adversaries.](#)

Thus, our work takes place in a programming-language setting, and it draws on a line of research on protection in programming languages, and more broadly on ideas and techniques from programming-language theory (e.g., [Morris 1973; Abadi 1998]). These include the use of contexts for representing attackers, and of contextual equivalence and similar relations for expressing security properties. Remarkably, though, this line of research has said little on randomization; a notable exception is the work of Pucella and Schneider [2006], which we describe further in Section 6. In addition, our probabilistic results are analogous to computational-soundness theorems in the analysis of security protocols (e.g., [Abadi and Rogaway 2002; Comon-Lundh and Cortier 2008; Backes et al. 2009]). These theorems re-

late symbolic proofs of protocol security, in which keys and ciphertexts are formal expressions, to proofs in a computational model in which keys and ciphertexts are bitstrings subject to complexity-theoretic assumptions. Unlike security protocols, however, the systems that we consider neither include concurrency nor rely on cryptography, but they do include higher-order functions. Despite these important differences, we hope that our work will enrich the study of computational soundness, in particular by showing that some of its themes and methods are applicable beyond security protocols.

The next section (Section 2) discusses our results in more detail but still informally. Section 3 contains preliminary technical material. Sections 4 and 5 are the core of the paper; they treat models in which errors are fatal and recoverable (but costly), respectively. Section 6 concludes.

2. DISCUSSION OF RESULTS

Layout randomization can be applied in a variety of systems contexts. In some (in particular, in kernel mode), accesses to unmapped memory addresses may be fatal violations that result in immediate termination. In others (often in user mode), erroneous accesses may take place repeatedly without causing execution to abort; a program that performs an erroneous access may often recognize that it has done so.

This distinction leads to two models for what happens when an attacker accesses an unused address in data memory (rather than an address that houses a private location). In one model, such accesses are fatal violations; in the other, such accesses are not fatal and can be detected.

In both cases, our main results concern translations between the high-level language with locations and a lower-level language with natural-number addresses. (In contrast, one could study layout randomization by focusing exclusively on low-level behavior, as in [Berger and Zorn 2006, Section 6].) In the high-level language, there is a distinct type of locations `loc` and, assuming that the expression M has this type, one can write expressions like $!_{\text{loc}}M$ and $M :=_{\text{loc}} M'$ for reading from and storing into a location. In the low-level language, on the other hand, if M has type `nat` then one can write $!_{\text{nat}}M$ and $M :=_{\text{nat}} M'$ for reading from and writing to a natural-number address, which may be obtained as the result of arbitrary numerical computations in M .

In order to study the security of these translations, we represent high- and low-level attackers as contexts. More precisely, for a program M of type σ , we take attackers to be expressions C of type $\sigma \rightarrow \text{bool}$. The boolean output is standard in programming-language theory, but technically we could as well use the type $\sigma \rightarrow \text{nat}$, for example. Informally, we think of C as interacting with M and possibly trying to obtain information about the contents of private locations or to tamper with them. Attackers must not have direct access to the private locations, so we consider only public attackers C , which are those containing no occurrences of any private locations. (Public low-level attackers do have access to all of memory, nevertheless, but via natural-number addresses rather than via locations.)

This representation of the attacker as a context amounts to a threat model, which allows rich interactions between the program being protected and its attacker. Both

theoretical work and practical attacks often employ more limited threat models, in which, for example, the attacker provides only one input or a small number of inputs. On the other hand, this representation excludes power-analysis attacks, timing-analysis attacks, and the like, as well as any attacks that subvert the underlying execution platform. Realistically, layout randomization may not withstand such attacks anyway.

In general, a low-level attacker could exploit the operations $!_{\text{nat}}$ and $:=_{\text{nat}}$ for crafting attacks that would be impossible in the high-level language. In an extreme case, when erroneous accesses are not fatal, an attacker could iterate over all addresses.

Nevertheless, we show that the attacks possible in the low-level language are no worse than those that are possible in the high-level language, in a probabilistic sense and, if erroneous accesses are not fatal, within some number of such memory accesses that serves as a bound on the complexity of the attacks. More precisely, we map each high-level program M to a low-level program M^\downarrow , and consider the behavior of M^\downarrow in an arbitrary low-level context C . We construct a corresponding high-level context C^\uparrow which does not directly access M 's private locations and is such that M in C^\uparrow exhibits the same behavior as M^\downarrow does in C (possibly in a probabilistic sense). In the model where erroneous accesses are fatal, $C^\uparrow M$ returns a given boolean, for example, if, and only if, CM^\downarrow does (see Theorem 4.5). In the model where erroneous accesses are not fatal, one rather has that $C^\uparrow M$ returns a given boolean if, and only if, CM^\downarrow does with high probability, and then only with an assumption on the number of memory accesses being bounded (see Theorem 5.7).

Some of our results are phrased as full abstraction theorems for translations between the high-level language with locations and the lower-level language with natural-number addresses (Theorems 4.7 and 5.8). These theorems say, roughly, that two programs are equivalent in the high-level language if, and only if, their translations are equivalent (in a probabilistic sense) in the low-level language. (Computational soundness is the implication from the high-level equivalence to the low-level one.) The equivalences capture program indistinguishability in the presence of an arbitrary attacker, represented as the context of the programs: the attacker cannot, for example, force the two programs to yield different values (possibly in a probabilistic sense). As the examples below illustrate, the equivalences can express both secrecy and integrity properties. Therefore, the theorems imply the preservation of those secrecy and integrity properties.

The distinction of public and private locations, and the use of equivalences, are similar to those in information-flow security. There, equivalences generally relate two versions of the same program with different inputs (e.g., [Denning 1982; Volpano et al. 1996]). Furthermore, preservation results are generally restricted to equivalences that can be established using particular logics or type systems (see, e.g., [Abadi 1999, Section 7] and [Medel 2006; Barthe et al. 2007; Fournet and Rezk 2008]). Like Fournet et al. [2009], we do not make such restrictions: our equivalences may relate programs that differ in more than their inputs, and they need not be proved with any particular method.

Consider the following simple programs M and M' of the high-level language:

$$M = l := c \quad M' = l := c'$$

where l is a private location and c and c' are two distinct natural-number constants. Here and in other examples, we omit the subscript `loc` on memory operations, for brevity. These programs have type `unit` (the type conventionally used in functional programming for commands). They can be distinguished by the context

$$\lambda g:\text{unit}. (g; \text{if } !l = c \text{ then true else false})$$

but they are equivalent with respect to contexts that cannot access private locations. This property captures a secrecy guarantee. Similarly, if l' is a public location, the following programs M and M' :

$$\begin{aligned} M &= \lambda f:\text{nat} \rightarrow \text{unit}. l := c; f(c); \\ &\quad \text{if } !l = c \text{ then } l' := c \text{ else } l' := c' \\ M' &= \lambda f:\text{nat} \rightarrow \text{unit}. l := c; f(c); \\ &\quad l' := c \end{aligned}$$

can be distinguished by the context

$$\lambda g:(\text{nat} \rightarrow \text{unit}) \rightarrow \text{unit}. (g(\lambda x:\text{nat}. l := c'); \text{if } !l' = c \text{ then true else false})$$

but they are equivalent with respect to contexts that cannot access private locations, because the argument f (supplied by the context) cannot tamper with l . This property captures an integrity guarantee. In an implementation in which l is housed in a random address in data memory, an attacker should find it hard to read or write the contents of l , so the secrecy and integrity guarantees should be preserved. We prove that this is indeed the case.

Such a result may seem obvious. However, as we discuss, some other “equally obvious” results do not hold, and some variants and extensions appear problematic. We illustrate this point with the following small example. Writing Ω for a chosen nonterminating program and $*$ for the value of type `unit`, we consider the programs:

$$\begin{aligned} M &= \lambda f:\text{unit} \rightarrow \text{unit}. \Omega \\ M' &= \lambda f:\text{unit} \rightarrow \text{unit}. \text{let } x \text{ be } f(*) \text{ in } \Omega \end{aligned}$$

The implementations of M and M' can be distinguished by a context that passes a function f that always produces a fatal error. Such a function can easily be expressed in the model where erroneous accesses are fatal. On the other hand, M and M' will be equivalent in the high-level language unless this language too includes constructs that force immediate termination. Therefore, full abstraction fails without such constructs. Although of mostly theoretical interest, this small example is reminiscent of some actual attacks in which the distinction between error and nontermination leaks important information [Sovarel et al. 2005].

Thus, our work demonstrates that layout randomization can offer some delicate but strong guarantees. Layout randomization is not just an ad hoc mitigation, or “security by obscurity”. Nevertheless, our results have substantial limitations. They provide an incomplete account of software protection, ignoring most of the complications of practical implementations.

Many of the limitations directly correspond to limitations of the languages that we consider. For instance, these languages do not include the storage of functions in the heap, which our results do not treat; so we do not study whether an attacker can

call a piece of code by guessing where in memory it resides, as in “jump-to-libc” attacks [Erlingsson 2007].

Another limitation of our results is that they do not all apply to programs that receive or send locations—although they do apply to higher-order programs that receive or send functions for manipulating locations. We deliberately define our languages with locations as first-class values of a type `loc`. While this generality leads to an extra hypothesis in some of our theorems, it also enables us to discuss the difficulties that arise with locations as first-class values:

— Suppose that we allow `loc` to occur in contravariant positions in the types of the programs that we are protecting (that is, we allow `loc` to occur within an odd number of left-hand sides of function-space arrows, as in, for example, `loc → unit` and `((loc → unit) → unit) → unit`). In the implementations of those programs, locations correspond to natural numbers, but in general this correspondence is not surjective. So a low-level attacker may attempt to poison the programs by providing a number that does not represent a location instead of one that does represent a location, and might gain information from the resulting errors. Consider for instance the programs:

$$M = \lambda x:\text{loc}.\Omega \quad M' = \lambda x:\text{loc}.\text{let } y \text{ be } !x \text{ in } \Omega$$

which both have type `loc → unit`. While a high-level attacker cannot distinguish these two programs, a low-level attacker may attempt to distinguish them, with high probability, by passing a number that does not represent a public location: the naive implementation of M will diverge, that of M' will produce an error. Such examples might be addressed by an implementation strategy in which incoming numbers that should represent locations are tested. These tests are reminiscent of how pointers are treated with suspicion when they cross trust boundaries in operating systems and other software systems.

— Suppose that we allow `loc` to occur in covariant positions in the types of the programs that we are protecting (that is, we allow `loc` to occur within an even number of left-hand sides of function-space arrows, as in, for example, `loc` and `(loc → unit) → unit`). Then a low-level attacker may store the numbers that represent the locations that it receives, and use them later, while analogous storage is not possible for a high-level attacker—simply because locations cannot hold locations in our high-level model. Letting l_1 and l_2 be private locations, consider for instance the programs:

$$\begin{aligned} M &= \lambda f:\text{loc} \rightarrow \text{unit}.\text{if } !l_2 = 0 \text{ then } l_2 := 1; f(l_1) \text{ else } \Omega; \\ &\quad l_1 := 0 \\ M' &= \lambda f:\text{loc} \rightarrow \text{unit}.\text{if } !l_2 = 0 \text{ then } l_2 := 1; f(l_1) \text{ else } \Omega; \\ &\quad l_1 := 1 \end{aligned}$$

which both have type `(loc → unit) → unit`. They differ only in whether they store 0 or 1 in l_1 . Both of these leak l_1 to an argument function f , then set l_1 . They do the leaking at most once: this linearity is enforced by the flag l_2 . A low-level context can store the number that represents l_1 , then use it for reading what is stored in l_1 , and thereby could distinguish the implementations of M and M' if no additional precautions are taken. This counterexample is reminiscent of some

that arise in the study of cryptographic protocols, most notably a counterexample to forward secrecy [Abadi 1998]. It could perhaps be addressed by some of the techniques developed in such contexts. Unfortunately, those techniques may not result in attractive, realistic implementation strategies for a programming language such as ours, or for its obvious extensions where locations can hold other locations. Such extensions can bring up further problems, which it would be interesting to investigate in future research.

The significance of this limitation remains open to debate. One could argue that programs should never receive or send locations, that it is too hard to make this safe, and that exchanging functions, or objects with public methods and private fields, provides more flexibility—*assuming that an attacker cannot recover the locations in question from the concrete representations of functions and objects. (Such a guarantee might be provided statically by typing or dynamically, perhaps via capabilities.)*

These arguments are particularly sensible in the context of implementations where attackers have information on the offsets between private locations (much as in [Shacham et al. 2004]). For instance, a practical implementation may well store several private locations near one another, in a randomly placed block of memory chosen for this purpose. Then an attacker that learns where l_1 is housed may also be able to infer that l_2 is nearby. Such dependencies can weaken security.

3. TECHNICAL PRELIMINARIES

This section presents basic technical material on which both Sections 4 and 5 rely. It describes *both* high- and low-level memory models and the common components of the languages considered in this paper.

3.1 Memory models

We begin with a discussion of our memory models. We need two: an abstract one, for the high-level language, and a more concrete one, for the low-level language.

For the abstract model we assume a finite set Loc of *locations*, ranged over by l , and further assumed to be the disjoint union of two sets, PubLoc and PriLoc , of *public* and *private* locations. *Stores*, ranged over by s , are maps $s : \text{Loc} \rightarrow \mathbb{N}$, sending locations to natural numbers. *For any store s , $s(l) = n$ indicates that the contents of the location l in the store s is n .* We write Store for the set of stores.

For the concrete model we take the memory as having addresses $0, \dots, c$, for a given $c \geq 0$, which we think of as logical or virtual addresses rather than physical addresses; we assume that $|\text{Loc}| \leq c + 1$. *Memories*, ranged over by m , are maps $m : \{0, \dots, c\} \rightarrow \mathbb{N} + \mathbb{1}$, where $\mathbb{1}$ is the set $\{*\}$, $+$ is disjoint union. *For any memory m , $m(a) = n$ indicates that the memory m holds n at address a , and $m(a) = *$ indicates that a is an unused address of m .* Storing natural numbers rather than words is an idealization, as is the view of natural numbers as atomic entities that all occupy the same space. With a little more effort we could use an alternative model where words are stored and arithmetic operations can be performed on them.

Memory layouts, ranged over by w , are 1-1 maps $w : \text{Loc} \hookrightarrow \{0, \dots, c\}$. They are used to connect the abstract and concrete memory models. *Note that, whereas there are infinitely many stores and memories, there are only a finite number of*

memory layouts. We consider only those memory layouts extending a given *public layout* $w_p: \text{PubLoc} \hookrightarrow \{0, \dots, c\}$ that is fixed throughout.

For any store s and a memory layout w , there is a corresponding memory $\text{mem}(s, w)$ defined by:

$$\text{mem}(s, w)(a) = \begin{cases} s(l) & \text{if } w(l) = a \\ * & \text{if } a \notin \text{Ran}(w) \end{cases}$$

where $\text{Ran}(w)$ is the range of w . Note that the map $s \mapsto \text{mem}(s, w)$ is 1-1, but not onto; we say that m has the form $\text{mem}(s, w)$ if it equals $\text{mem}(s, w)$ for some w . We abbreviate $\text{mem}(s, w)$ to s_w .

In order to make probabilistic assertions, we need a probability distribution d over the layouts extending w_p . We mainly consider the uniform probability distribution U , which can be generated by fixing an ordering of Loc and then selecting, one-by-one, a non-repeating sequence of elements randomly from $\{0, \dots, c\} \setminus \text{Ran}(w_p)$, choosing uniformly at each point from the remaining elements. When $\varphi(w)$ is a statement, we write $P_d(\varphi(w))$ for the probability that it holds with respect to the distribution d .

For any $A \subseteq \mathbb{N}$, we define $w \# A$ to mean that $A \cap (\text{Ran}(w) \setminus \text{Ran}(w_p)) = \emptyset$. Thinking of A as a set of attempted memory probes, allowing probes out of memory bounds and at addresses of public locations, $w \# A$ holds precisely when none of these probes hits private memory, that is, an address of a private location. $P_d(w \# A)$ is then the probability that no probe in A hits private memory, using the above convention on probabilistic statements with free variable w . We define:

$$\delta_d(n) = \min\{P_d(w \# A) \mid A \subseteq \{0, \dots, c\} \setminus \text{Ran}(w_p), |A| = n\}$$

for $n \leq c + 1 - |\text{PubLoc}|$ (and $\delta_d(n)$ is undefined otherwise). This number will be used to give our security guarantees. Note that $\delta_d(0) = 1$ and that if $\delta_d(n) > 0$ then $n \leq c + 1 - |\text{Loc}|$, although the converse may fail.

In the case of the uniform distribution, $P_U(w \# A)$ depends only on the cardinality of A , if $A \subseteq \{0, \dots, c\} \setminus \text{Ran}(w_p)$, so we have $\delta_U(n) = P_U(w \# A)$, for any such A with $|A| = n$. Note that $\delta_U(n) > 0$ if, and only if, $n \leq c + 1 - |\text{Loc}|$.

We can give $\delta_U(n)$ by:

$$\delta_U(n) = \frac{|\{w \mid w \text{ extends } w_p \text{ and } \text{Ran}(w) \cap A = \emptyset\}|}{|\{w \mid w \text{ extends } w_p\}|}$$

where A is any subset of $\{0, \dots, c\} \setminus \text{Ran}(w_p)$ of size n ; it does not matter which such subset is chosen. That is, $\delta_U(n)$ is the fraction of all memory layouts that extend w_p but do not meet any of a fixed choice of n non-public memory addresses. The formula can be written in terms of binomial coefficients, by:

$$\delta_U(n) = \binom{c + 1 - n - |\text{PubLoc}|}{|\text{PriLoc}|} / \binom{c + 1 - |\text{PubLoc}|}{|\text{PriLoc}|}$$

Thus, $\delta_U(n)$ tends to 1 as c increases while PriLoc and PubLoc remain fixed. Intuitively, this fact means that, if one looks for private locations in a large enough memory, getting n tries, one is almost certain to miss if the memory is large enough.

As is shown by the following calculation, the uniform distribution U is optimal in the sense that $\delta_U \geq \delta_d$ for any probability distribution d ; that is, U maximizes δ_d .

The second step of the calculation uses the fact that the minimum of a nonempty finite multiset of real numbers is less than, or equal to, their average.

$$\begin{aligned}
\delta_d(n) &=_{\text{def}} \min\{P_d(w\#A) \mid A \subseteq \{0, \dots, c\} \setminus \text{Ran}(w_p), |A| = n\} \\
&\leq \sum\{P_d(w\#A) \mid A \subseteq \{0, \dots, c\} \setminus \text{Ran}(w_p), |A| = n\} / \binom{c+1-|\text{PubLoc}|}{n} \\
&= \sum\{(\sum_{w\#A} d(w)) \mid A \subseteq \{0, \dots, c\} \setminus \text{Ran}(w_p), |A| = n\} / \binom{c+1-|\text{PubLoc}|}{n} \\
&= \sum_w \sum\{d(w) \mid A \subseteq \{0, \dots, c\} \setminus \text{Ran}(w), |A| = n\} / \binom{c+1-|\text{PubLoc}|}{n} \\
&= \sum_w d(w) \binom{c+1-|\text{Loc}|}{n} / \binom{c+1-|\text{PubLoc}|}{n} \\
&= \binom{c+1-|\text{Loc}|}{n} / \binom{c+1-|\text{PubLoc}|}{n} \\
&= \frac{(c+1-|\text{Loc}|)!(c+1-n-|\text{PubLoc}|!)}{(c+1-n-|\text{Loc}|)!(c+1-|\text{PubLoc}|!)} \\
&= \binom{c+1-n-|\text{PubLoc}|}{|\text{PriLoc}|} / \binom{c+1-|\text{PubLoc}|}{|\text{PriLoc}|} \\
&= \delta_U(n)
\end{aligned}$$

Non-uniform distributions may still be helpful in practice. For example, one may wish to restrict randomization to part of the memory, or to restrict layouts (for example mapping some locations into a special memory region), and it may be possible to obtain sufficient security guarantees under such restrictions.

Our results hold for any probability distribution d . For the rest of the paper, we fix a choice of d , and write $P(\varphi(w))$ and δ_n for $P_d(\varphi(w))$ and $\delta_d(n)$, respectively.

3.2 Languages

A number of quite similar languages are considered in this paper. They are all versions of Moggi's (call-by-value) computational λ -calculus, or λ_c -calculus, [Moggi 1989; 1991] with natural number and, possibly, location types, and with memory-access operations at natural-number or location types. They all also have sum types, which represent disjoint or discriminated unions [Mitchell 1996], and recursion [Hasegawa and Kakutani 2002]. [For general background on the \$\lambda\$ -calculus, see \[Mitchell 1996; Pierce 2002\].](#)

The types of such a language are given by:

$$\sigma ::= b \mid \mathbf{unit} \mid \sigma \times \sigma \mid \sigma + \sigma \mid \sigma \rightarrow \sigma$$

where b ranges over a given set of *basic* types which always includes a natural-number type \mathbf{nat} and may also include a location type \mathbf{loc} . We write \mathbf{bool} to abbreviate $\mathbf{unit} + \mathbf{unit}$.

The terms of such a language are ranged over by M and N , and given by:

$$\begin{aligned} M ::= & x \mid c \mid * \mid (M, M) \mid \mathbf{fst} M \mid \mathbf{snd} M \mid \\ & \mathbf{inl}_{\sigma, \sigma} M \mid \mathbf{inr}_{\sigma, \sigma} M \mid \\ & \mathbf{cases} M \mathbf{inl} x : \sigma. M \mathbf{inr} x : \sigma. M \mid \\ & \lambda x : \sigma. M \mid MM \mid \mathbf{rec}(f : \sigma \rightarrow \tau, x : \sigma). M \end{aligned}$$

where c ranges over a given set of constants c of a given type σ , written $c : \sigma$. These always include the natural numbers $n \in \mathbb{N}$, together with constants for the usual arithmetic operations and relations, such as addition $+ : \mathbf{nat} \times \mathbf{nat} \rightarrow \mathbf{nat}$ and equality $=_{\mathbf{nat}} : \mathbf{nat} \times \mathbf{nat} \rightarrow \mathbf{bool}$. They may also include constants for memory access, for example $:=_{\mathbf{nat}} : \mathbf{loc} \times \mathbf{nat} \rightarrow \mathbf{unit}$ for assignment. The recursion construction $\mathbf{rec}(f : \sigma \rightarrow \tau, x : \sigma). M$ should be thought of as defining a function $f : \sigma \rightarrow \tau$ such that $f(x) = M$.

There are standard notions of free and bound variables, of closed terms, and of the capture-avoiding substitution $M[N/x]$ of a term N for all free occurrences of a variable x in a term M . There are also standard typing rules for judgements $\Gamma \vdash M : \sigma$, that a term M has type σ in the context Γ , where contexts have the form $\Gamma = x_1 : \sigma_1, \dots, x_n : \sigma_n$. Here are two examples:

$$\frac{\Gamma \vdash M : \sigma}{\Gamma \vdash \mathbf{inl}_{\sigma, \tau} M : \sigma + \tau} \quad \frac{\Gamma, f : \sigma \rightarrow \tau, x : \sigma \vdash M : \tau}{\Gamma \vdash \mathbf{rec}(f : \sigma \rightarrow \tau, x : \sigma). M : \sigma \rightarrow \tau}$$

We write $M : \sigma$ for $\Gamma \vdash M : \sigma$, and read it as “ M (is) of type σ ”; one can think of σ as the interface of the term M . If $M : \sigma$ then we say that M is well-typed (and it is necessarily closed). Unique typing holds: a term has at most one type relative to a given environment.

We may omit type subscripts when that should not cause confusion, e.g., we write $\mathbf{inl} M$ instead of $\mathbf{inl}_{\sigma, \tau} M$; we also write $\mathbf{let} x : \sigma \mathbf{be} M \mathbf{in} N$ for $(\lambda x : \sigma. N)M$. We adopt standard infix notations, e.g., writing $M := N$ for $:=(M, N)$, if that improves readability. For the booleans, we write \mathbf{true} and \mathbf{false} for $\mathbf{inl} *$ and $\mathbf{inr} *$, respectively, and the conditional expression $\mathbf{if} B \mathbf{then} M \mathbf{else} N$ abbreviates $\mathbf{cases} B \mathbf{inl} x : \mathbf{unit}. M \mathbf{inr} x : \mathbf{unit}. N$, where x occurs free in neither M nor N . To make the usual connection between applicative and imperative programs, we may write \mathbf{com} (which stands for “command”) for \mathbf{unit} , \mathbf{skip} for $*$, and $M; N$ for $\mathbf{let} x : \mathbf{unit} \mathbf{be} M \mathbf{in} N$ (where x is not free in N).

Throughout this paper, we define the operational semantics of such a language in the style of Felleisen and Friedman [1986], beginning by defining *values* V , *evaluation contexts* E , and *redexes* R . We classify each constant as a value or a redex; in particular the numerals and the constants for the assumed arithmetic operations and relations are always values; the error-raising constants of the high-level language of Section 4.1 are examples of constants which are not values. Values are terms which can be thought of as (syntax for) completed computations; they are ranged over by V and defined by:

$$\begin{aligned} V ::= & c \quad (\text{if } c \text{ is classified as a value}) \mid \\ & * \mid (V, V) \mid \mathbf{inl} V \mid \mathbf{inr} V \mid \lambda x : \sigma. M \end{aligned}$$

Evaluation contexts are ranged over by E and are defined by:

$$E ::= [-] \mid (E, M) \mid (V, E) \mid \mathbf{fst} E \mid \mathbf{snd} E \mid \\ \mathbf{inl} E \mid \mathbf{inr} E \mid \mathbf{cases} E \mathbf{inl} x:\sigma. M \mathbf{inr} x:\sigma. M \mid \\ EM \mid VE$$

We write $E[M]$ for the term obtained by replacing the “hole” $[-]$ in an evaluation context E by a term M . The computational thought behind evaluation contexts is that, in a term of the form $E[M]$, the first computational step arises within M . The redexes R include:

$$c \quad (\text{if } c \text{ is classified as a redex}) \\ \mathbf{fst}(V, V) \quad \mathbf{snd}(V, V) \\ \mathbf{cases} \mathbf{inl} V \mathbf{inl} x:\sigma. M \mathbf{inr} x:\sigma. M \\ \mathbf{cases} \mathbf{inr} V \mathbf{inl} x:\sigma. M \mathbf{inr} x:\sigma. M \\ (\lambda x:\sigma. M)V \quad \mathbf{rec}(f:\sigma \rightarrow \tau, x:\sigma). M$$

together with specified other redexes involving the various constants, including evident arithmetic redexes for the assumed arithmetic operations and relations, for example $i + j$ and $i =_{\text{nat}} j$.

For every well-typed term M , exactly one of the following possibilities holds:

- M is a value, or
- M can be analyzed uniquely in the form $E[R]$, with R a well-typed redex.

However, this has to be verified separately for each language.

The operational semantics itself involves various relations and properties, and there is quite a bit of variation between the different languages. In all cases, however, a relation $R \longrightarrow M$ between the above redexes and terms proves useful. It is defined as follows:

$$\mathbf{fst}(V, V') \longrightarrow V \quad \mathbf{snd}(V, V') \longrightarrow V' \\ (\lambda x:\sigma. M)V \longrightarrow M[V/x] \\ \mathbf{rec}(f:\sigma \rightarrow \tau, x:\sigma). M \longrightarrow \lambda x:\sigma. M[\mathbf{rec}(f:\sigma \rightarrow \tau, x:\sigma). M/f] \\ \dots$$

where the ellipses indicate evident missing arithmetic redex transitions, such as:

$$i =_{\text{nat}} i \longrightarrow \mathbf{true} \quad \text{and} \quad i + j \longrightarrow k$$

where k is the sum of i and j .

4. THE FATAL-ERROR MODEL

As explained in Section 2, our results compare the security properties implied by the semantics of high-level languages with those obtained from layout randomization in low-level languages. In this section, relying on the general framework of Section 3, we define a particular high-level language and a particular low-level language, then we develop the corresponding comparison.

Specifically, we consider a low-level model in which an erroneous memory access gives rise to a fatal error (that is, an irrecoverable error). A corresponding construct to raise such errors is needed at high-level, as discussed in Section 2. Section 4.1 presents the high-level language; since it has an error-raising construction, its operational semantics has an error predicate, as well as the usual transition relation. Section 4.2 presents the low-level language; its operational semantics additionally has an error predicate which is labelled by a natural number; this instrumentation is used in proofs and is not regarded as part of program behavior.

To mediate between the two languages, an instrumented high-level language, extending the high-level language, is given in Section 4.3. This language has facilities for memory access at both location and natural-number types, with non-public natural-number accesses always raising an instrumented error. We have a behavior-preserving translation to the high-level language and a translation to the low-level language which is additionally sensitive to the instrumentation.

With these tools, we prove our main results for the fatal-error model in Sections 4.4 and 4.5, to the effect that high- and low-level attackers have essentially equal power, modulo the translation from the high-level to the low-level language, and that this translation preserves and reflects suitable notions of contextual equivalence. In both this and the following section, we often omit, or abbreviate, routine proofs.

4.1 The high-level language

The high-level language employs the abstract notion of location. The basic types are `nat` and `loc`. The constants are the arithmetic constants, together with error-raising constants:

$$\text{raise_error}_\sigma : \sigma \quad (\text{for every } \sigma)$$

and constants for accessing and updating locations (which do not raise errors):

$$l_{\text{loc}} : \text{loc} \quad (l \in \text{Loc}) \quad !_{\text{loc}} : \text{loc} \rightarrow \text{nat} \quad :=_{\text{loc}} : \text{loc} \times \text{nat} \rightarrow \text{com}$$

Of these, the arithmetic constants and the constants for accesses and updating locations are values, and the error-raising constant is a redex. As well as the redexes specified by the general framework, there are the following two kinds:

$$!_{\text{loc}} V \quad V :=_{\text{loc}} V$$

For the semantics of the high-level language we define a *configuration* to be a pair (s, M) with s a store and M a well-typed term. The semantics then consists of a transition relation and an error property:

$$(s, M) \longrightarrow (s', M') \quad (s, M) \downarrow_{\text{error}}$$

The transition relation and the error property are obtained from the special case of redexes by two rules:

$$\frac{(s, R) \longrightarrow (s', M')}{(s, E[R]) \longrightarrow (s', E[M'])} \quad \frac{(s, R) \downarrow_{\text{error}}}{(s, E[R]) \downarrow_{\text{error}}}$$

For redexes we take the transitions to be given by:

$$(s, !_{\text{loc}} l_{\text{loc}}) \longrightarrow (s, n) \quad (\text{if } s(l) = n) \quad (s, l_{\text{loc}} :=_{\text{loc}} n) \longrightarrow (s[l \mapsto n], \text{skip})$$

and a rule:

$$\frac{R \longrightarrow M'}{(s, R) \longrightarrow (s, M')}$$

The error property is given by:

$$(s, \text{raise_error}_\sigma) \downarrow_{\text{error}}$$

The operational semantics is “small-step”; one can define a corresponding “big-step” semantics by:

$$\begin{aligned} (s, M) \Longrightarrow (s', V) &\iff (s, M) \rightarrow^* (s', V) \\ (s, M) \downarrow_{\text{error}} &\iff \exists s', M'. (s, M) \rightarrow^* (s', M') \downarrow_{\text{error}} \\ (s, M) \uparrow &\iff \forall n. \exists s', M'. (s, M) \rightarrow^n (s', M') \end{aligned}$$

Note that these relations and properties are mutually exclusive. The relation $(s, M) \Longrightarrow (s', V)$ holds if M evaluates to the value V with final store s' when the initial store is s ; the property $(s, M) \downarrow_{\text{error}}$ holds if the term M results in an error when the initial store is s ; the property $(s, M) \uparrow$ holds if M diverges when the initial store is s .

4.2 The low-level language

In the low-level language all memory accesses are made via natural numbers. Consequently we take the only basic type to be `nat`. (A possible variant would be to have a separate memory-address type.) As well as the arithmetic constants, the low-level language has error-raising constants:

$$\text{raise_error}_\sigma \quad (\text{for every } \sigma)$$

and memory-access constants:

$$l_{\text{nat}} : \text{nat} \quad (l \in \text{Loc}) \quad !_{\text{nat}} : \text{nat} \rightarrow \text{nat} \quad :=_{\text{nat}} : \text{nat} \times \text{nat} \rightarrow \text{com}$$

Note that `loc` cannot occur in σ in $\text{raise_error}_\sigma$ as it is not a low-level type, and also that there are constants l_{nat} for all the locations, not just the public ones. We say that a term is *public* if every l_{nat} that occurs in it has $l \in \text{PubLoc}$. We take $!_{\text{nat}}$ and $:=_{\text{nat}}$ to be values, and $\text{raise_error}_\sigma$ and l_{nat} (with $l \in \text{Loc}$) to be redexes. The other redexes are those specified by the general framework, together with:

$$!_{\text{nat}} V \quad V :=_{\text{nat}} V$$

Configurations in the low-level operational semantics are pairs (m, M) of a memory m and a well-typed term M . The semantics is defined relative to a choice of a memory layout: [this memory layout is needed to interpret the location constants \$l_{\text{nat}}\$, which here function as names for addresses](#). It consists of a transition relation and two error properties, all relative to the memory layout chosen:

$$w \models (m, M) \longrightarrow (m', M')$$

$$w \models (m, M) \downarrow_{\text{error}} \quad w \models (m, M) \downarrow_{\text{error}}^a$$

These are obtained from the special case of redexes much as above:

$$\frac{w \models (m, R) \longrightarrow (m', M')}{w \models (m, E[R]) \longrightarrow (m', E[M'])}$$

$$\frac{w \models (m, R) \Downarrow_{\text{error}}}{w \models (m, E[R]) \Downarrow_{\text{error}}} \qquad \frac{w \models (m, R) \Downarrow_{\text{error}}^a}{w \models (m, E[R]) \Downarrow_{\text{error}}^a}$$

For the redexes, the transitions and error properties are given by the rule:

$$\frac{R \longrightarrow M'}{w \models (m, R) \longrightarrow (m, M')}$$

together with:

$$w \models (m, \text{raise_error}_\sigma) \Downarrow_{\text{error}} \qquad w \models (m, l_{\text{nat}}) \longrightarrow (m, w(l)) \quad (l \in \text{Loc})$$

and:

$$\begin{aligned} w \models (m, !_{\text{nat}} a) \longrightarrow (m, n) & \quad (\text{if } a \in \{0, \dots, c\} \text{ and } m(a) = n) \\ w \models (m, !_{\text{nat}} a) \Downarrow_{\text{error}}^a & \quad (\text{if } a \notin \{0, \dots, c\} \text{ or } m(a) = *) \end{aligned}$$

and:

$$\begin{aligned} w \models (m, a :=_{\text{nat}} n) \longrightarrow (m[a \mapsto n], \text{skip}) & \quad (\text{if } a \in \{0, \dots, c\} \text{ and } m(a) \neq *) \\ w \models (m, a :=_{\text{nat}} n) \Downarrow_{\text{error}}^a & \quad (\text{if } a \notin \{0, \dots, c\} \text{ or } m(a) = *) \end{aligned}$$

The low-level big-step operational semantics is defined by:

$$\begin{aligned} w \models (m, M) \Longrightarrow (m', V) & \iff w \models (m, M) \rightarrow^* (m', V) \\ w \models (m, M) \Downarrow_{\text{error}}^a & \iff \exists m', M'. w \models (m, M) \rightarrow^* (m', M') \Downarrow_{\text{error}}^a \\ w \models (m, M) \Downarrow_{\text{error}} & \iff \exists m', M'. w \models (m, M) \rightarrow^* (m', M') \Downarrow_{\text{error}} \\ w \models (m, M) \Uparrow & \iff \forall n. \exists m', M'. w \models (m, M) \rightarrow^n (m', M') \end{aligned}$$

As is the case at the high-level, these relations and properties are mutually exclusive. Note that if $w \models (m, M) \Downarrow_{\text{error}}^a$ or $w \models (m, M) \Downarrow_{\text{error}}^a$, and m has the form s_w , then $a \notin \text{Ran}(w_p)$.

4.3 The instrumented high-level language

In order to relate the high-level semantics uniformly to the low-level language we instrument it by adding some constants for accessing the store at type `nat`; in the final analysis, these will be translated away. Thus, the instrumented high-level language serves as a stepping stone, with semantics that resembles that of the high-level language but with a syntax that includes low-level constructs.

The instrumented high-level language has the same basic types as the high-level language and its constants are those of the high-level language together with:

$$l_{\text{nat}} : \text{nat} \quad (l \in \text{PubLoc}) \quad !_{\text{nat}} : \text{nat} \rightarrow \text{nat} \quad :=_{\text{nat}} : \text{nat} \times \text{nat} \rightarrow \text{com}$$

We take l_{nat} to be a redex (for $l \in \text{PubLoc}$), and $!_{\text{nat}}$ and $:=_{\text{nat}}$ to be values, and classify the other constants as in the case of the high-level language. The other redexes are those specified by the general framework, together with the following ones:

$$!_{\text{nat}} V \quad V :=_{\text{nat}} V \qquad !_{\text{loc}} V \quad V :=_{\text{loc}} V$$

the latter two kinds being inherited from the high-level language.

For the operational semantics, configurations are defined as for the high-level language, but we add an instrumented error property:

$$(s, M) \Downarrow_{\text{error}}^a \quad (a \in \mathbb{N})$$

We then proceed as for the high-level language, adding a rule for the instrumented error property:

$$\frac{(s, R) \Downarrow_{\text{error}}^a}{(s, E[R]) \Downarrow_{\text{error}}^a}$$

redex transitions:

$$\begin{aligned} (s, l_{\text{nat}}) &\longrightarrow (s, w_p(l)) && (l \in \text{PubLoc}) \\ (s, !_{\text{nat}} a) &\longrightarrow (s, s(l)) && (l \in \text{PubLoc}, a = w_p(l)) \\ (s, a :=_{\text{nat}} n) &\longrightarrow (s[l \mapsto n], \text{skip}) && (l \in \text{PubLoc}, a = w_p(l)) \end{aligned}$$

and instrumented error properties:

$$(s, !_{\text{nat}} a) \Downarrow_{\text{error}}^a \quad (s, a :=_{\text{nat}} n) \Downarrow_{\text{error}}^a \quad (a \notin \text{Ran}(w_p))$$

For the big-step semantics one defines one more predicate:

$$(s, M) \Downarrow_{\text{error}}^a \iff \exists s', M'. (s, M) \rightarrow^* (s', M') \Downarrow_{\text{error}}^a$$

Note that if $(s, M) \Downarrow_{\text{error}}^a$ or $(s, M) \Downarrow_{\text{error}}^a$ then $a \notin \text{Ran}(w_p)$. Note too that the operational semantics of the instrumented high-level language is conservative over that of the high-level language. For the small-step operational semantics, [conservativity](#) means that, for any high-level terms M and N , a transition $(s, M) \longrightarrow (s', M')$ holds in the high-level language if, and only if, it does in the instrumented high-level language, and similarly for properties $(s, M) \Downarrow_{\text{error}}$. Conservativity then follows for the big-step operational semantics: for every high-level term M and value V , $(s, M) \Longrightarrow (s', V)$, $(s, M) \Downarrow_{\text{error}}$, or $(s, M) \Uparrow$ hold in the high-level language if, and only if, they do in the instrumented one.

4.3.1 Translating instrumented high-level to high-level. Every term $M : \sigma$ of the instrumented high-level language can be translated to a term $M^\uparrow : \sigma$ of the high-level language. First we need a function to convert addresses of public locations to the locations themselves. Let $l^{(1)}, \dots, l^{(p)}$ be a listing without repetitions of PubLoc , and set $a_i =_{\text{def}} w_p(l^{(i)})$, for $i = 1, p$. Define the high-level term $G : \text{nat} \rightarrow \text{loc}$ to be:

$$\begin{aligned} \lambda x : \text{nat}. & \text{if } x = a_1 \text{ then } (l^{(1)})_{\text{loc}} \\ & \text{elseif } x = a_2 \text{ then } (l^{(2)})_{\text{loc}} \\ & \quad \vdots \\ & \text{elseif } x = a_p \text{ then } (l^{(p)})_{\text{loc}} \\ & \text{else raise_error}_{\text{loc}} \end{aligned}$$

with the evident understanding of the multiple conditional.

Then the translation is given by replacing the additional constants of the instrumented high-level language as follows:

$$\begin{aligned} l_{\text{nat}}^\uparrow &= w_p(l) \quad (l \in \text{PubLoc}) \\ !_{\text{nat}}^\uparrow &= \lambda x : \text{nat}. !_{\text{loc}} Gx \\ :=_{\text{nat}}^\uparrow &= \lambda x : \text{nat} \times \text{nat}. G(\text{fst } x) :=_{\text{loc}} (\text{snd } x) \end{aligned}$$

and leaving the other constants fixed. [The idea of this translation is to raise an error whenever the low-level term makes a non-public memory access.](#)

Define $(s, M) \Downarrow_{\text{error}}^u$ to hold if, and only if, either $(s, M) \Downarrow_{\text{error}}$ holds or else $(s, M) \Downarrow_{\text{error}}^a$ does, for some a . (The $\Downarrow_{\text{error}}^u$ property can be read as “**uninstrumented error**”.) Then the translation is correct for the big-step semantics in the following sense:

PROPOSITION 4.1. *Let M be a well-typed term of the instrumented high-level language. Then:*

- (1) *If $(s, M) \Longrightarrow (s', V)$ then $(s, M^\uparrow) \Longrightarrow (s', V^\uparrow)$.*
- (2) *If $(s, M) \Downarrow_{\text{error}}^u$ then $(s, M^\uparrow) \Downarrow_{\text{error}}$.*
- (3) *If $(s, M) \Uparrow$ then $(s, M^\uparrow) \Uparrow$.*

4.3.2 *Translating instrumented high-level to low-level.* We translate types σ and terms $M:\sigma$ of the instrumented high-level language to types σ^\downarrow and terms $M^\downarrow:\sigma^\downarrow$ of the low-level language. For types we replace all occurrences of `loc` by `nat`. For terms we replace each occurrence of a type σ by one of σ^\downarrow , and translate the constants as follows:

$$\begin{aligned} (l_{\text{loc}})^\downarrow &= l_{\text{nat}} \\ (!_{\text{loc}})^\downarrow &= !_{\text{nat}} \\ (:=_{\text{loc}})^\downarrow &= :=_{\text{nat}} \\ (\text{raise_error}_\sigma)^\downarrow &= \text{raise_error}_{\sigma^\downarrow} \end{aligned}$$

taking the translation to act as the identity on l_{nat} ($l \in \text{PubLoc}$), $!_{\text{nat}}$ and $:=_{\text{loc}}$.

The translation is correct with respect to the low-level semantics, in the sense, roughly, that M^\downarrow simulates M . However there is a small problem in that the translation of a location value is not a natural-number value but, rather, is a natural-number redex. For that reason a translation of a term of a given type, e.g., l_{loc} , can make a transition to a term not itself a translation of a term of that type.

In order to track this correspondence between high-level and low-level terms, we define a simulation relation $M \searrow_w N$ between terms of the instrumented high-level language and the low-level language, parameterized on a memory layout w . We take this relation to be the least relation between terms of the instrumented high-level language and the low-level language which includes:

$$c \searrow_w c^\downarrow \quad l_{\text{loc}} \searrow_w w(l)$$

and which is closed under the other language constructs, meaning that, for example:

- if $M_1 \searrow_w N_1$ and $M_2 \searrow_w N_2$ then $M_1 M_2 \searrow_w N_1 N_2$, and
- if $M \searrow_w N$ then $\lambda x:\sigma. M \searrow_w \lambda x:\sigma^\downarrow. N$.

For any term M of the instrumented high-level language we have $M \searrow_w M^\downarrow$; further, if $M:\sigma$ and $M \searrow_w N$ then $N:\sigma^\downarrow$.

We have the following big-step simulation lemma:

PROPOSITION 4.2. *Suppose that $M \searrow_w N$ for well-typed terms M of the instrumented high-level language and N of the low-level language. Then:*

- (1) *If $(s, M) \Longrightarrow (s', V)$, then there is a V' , with $V \searrow_w V'$, such that $w \models (s_w, N) \Longrightarrow (s'_w, V')$.*
- (2) *If $(s, M) \Downarrow_{\text{error}}$ then $w \models (s_w, N) \Downarrow_{\text{error}}$.*

- (3) If $(s, M) \Downarrow_{\text{error}}^a$ then, if $a \notin \text{Ran}(w)$, $w \models (s_w, N) \Downarrow_{\text{error}}^a$.
- (4) If $(s, M) \Uparrow$ then, for any w , $w \models (s_w, N) \Uparrow$.

The third case is particularly important as it enables one to use the instrumented high-level language to track memory accesses in the low-level language largely independently of memory layout.

4.4 High- and low-level attackers

We are now in a position to formulate our theorems for the fatal-error case. The general idea is to show that a program (taken to be a closed term) executed in the abstract memory model is equally secure if executed in the concrete one. In terms of our typed programming language we wish to show that a high-level term $M : \sigma$ is as secure as its low-level counterpart $M^\downarrow : \sigma^\downarrow$. We prove this if σ is `loc-free`, i.e., if $\sigma^\downarrow = \sigma$; [of course this restriction refers only to the interface of \$M\$: locations can still be used internally, that is, subterms of \$M\$ can have types with occurrences of `loc`. \(Our security result does not hold more generally—see the discussion in Section 2.\)](#)

In this section, we study the relation between high- and low-level attackers, [represented as contexts](#). In Section 4.5, we consider equivalences. Say that an instrumented high-level term (low-level term) is *public* if it contains no occurrence of any l_{loc} (respectively l_{nat}) with $l \in \text{PriLoc}$. We would like to show that attackers gain no advantage by attacking at low-level rather than at high-level. They certainly lose none, as, for any public high-level term $C : \sigma \rightarrow \text{bool}$, the low-level term C^\downarrow is of equal attacking power, [as the following proposition establishes](#):

PROPOSITION 4.3. *Let $M : \sigma$ be a high-level term and let $C : \sigma \rightarrow \text{bool}$ be a public high-level term. Then:*

- (1) If $(s, CM) \Longrightarrow (s', V)$ then, for any w , $w \models (s_w, C^\downarrow M^\downarrow) \Longrightarrow (s'_w, V)$.
- (2) If $(s, CM) \Downarrow_{\text{error}}$ then, for any w , $w \models (s_w, C^\downarrow M^\downarrow) \Downarrow_{\text{error}}$.
- (3) If $(s, CM) \Uparrow$ then, for any w , $w \models (s_w, C^\downarrow M^\downarrow) \Uparrow$.

These exhaust all possibilities for the big-step semantics of CM .

PROOF. That these are all the possibilities is simply because CM is a high-level term. That the statements concerning these possibilities hold is an immediate consequence of Proposition 4.2. (Note that, for any value $V : \text{bool}$, if $V \searrow_w V'$ then $V' = V$.) \square

We [restate this proposition](#) in terms of a convenient notion of evaluation function. For any store s and term $M : \sigma$ of the instrumented high-level language, and so also of the high-level language, define their *behavior* $\text{Eval}(M, s)$ by:

$$\text{Eval}(M, s) = \begin{cases} (s', V) & \text{if } (s, M) \Longrightarrow (s', V) \\ \text{error} & \text{if } (s, M) \Downarrow_{\text{error}}^u \\ \Omega & \text{if } (s, M) \Uparrow \end{cases}$$

Here `error` is a token indicating the raising of an error (and Ω is the divergent program chosen above).

Similarly, for any low-level term $M : \sigma$, memory m , and layout w define their *behavior* $\text{Eval}_w(M, m)$ by:

$$\text{Eval}_w(M, m) = \begin{cases} (m', V) & \text{if } w \models (m, M) \Longrightarrow (m', V) \\ \text{error} & \text{if } w \models (m, M) \Downarrow_{\text{error}}^u \\ \Omega & \text{if } w \models (m, M) \Uparrow \end{cases}$$

where $w \models (m, A) \Downarrow_{\text{error}}^u$ is defined to hold if, and only if, either $w \models (m, A) \Downarrow_{\text{error}}$ holds or $w \models (m, A) \Downarrow_{\text{error}}^a$ does, for some a .

We write x_w to mean (s_w, M) when x is (s, M) and x when x is error or Ω . [Proposition 4.3](#) is then equivalent to the following proposition:

PROPOSITION 4.4. *Let $M : \sigma$ be a high-level term and let $C : \sigma \rightarrow \text{bool}$ be a public high-level term. Then:*

$$\text{Eval}(CM, s)_w = \text{Eval}_w(C^\downarrow M^\downarrow, s_w)$$

for any store s and memory layout w .

For a converse, suppose now that $C : \sigma \rightarrow \text{bool}$ is a public low-level term (so, as above, σ is `loc`-free since low-level types do not contain `loc`). Then C is also a public instrumented high-level term, and we would like to show that the public high-level term $C^\uparrow : \sigma \rightarrow \text{bool}$ is an attacker of equal power. This will be true in a probabilistic sense:

THEOREM 4.5. *Suppose that $M : \sigma$ is a high-level term and $C : \sigma \rightarrow \text{bool}$ is a public low-level term. Then one of the following three mutually exclusive statements holds for any store s :*

- $\exists s', V. \forall w. w \models (s_w, CM^\downarrow) \Longrightarrow (s'_w, V)$ and $(s, C^\uparrow M) \Longrightarrow (s', V)$,
- $\text{P}(w \models (s_w, CM^\downarrow) \Downarrow_{\text{error}}^u) \geq \delta_1$ and $(s, C^\uparrow M) \Downarrow_{\text{error}}$, or
- $\forall w. w \models (s_w, CM^\downarrow) \Uparrow$ and $(s, C^\uparrow M) \Uparrow$.

PROOF. First note that $(CM)^\uparrow = C^\uparrow M^\uparrow = C^\uparrow M$ (as $M^\uparrow = M$) and also that $(CM)^\downarrow = C^\downarrow M^\downarrow = CM^\downarrow$ (as $C^\downarrow = C$). The proof now proceeds by considering the big-step behavior of (s, CM) . There are four mutually exclusive possibilities: we consider each of them in turn.

(1) In the first case we have $(s, CM) \Longrightarrow (s', V)$ for some s' and V . We then have $(s, C^\uparrow M) \Longrightarrow (s', V)$, by part 1 of Proposition 4.1. [Using part 1 of Proposition 4.2](#), we also have, for any w that $w \models (s_w, CM^\downarrow) \Longrightarrow (s'_w, V')$, for some V' with $V \searrow_w V'$; as V is a boolean value, we then have that V and V' are identical.

(2) In the second case we have $(s, CM) \Downarrow_{\text{error}}$, so, arguing as above but here using the second parts of the propositions, $(s, C^\uparrow M) \Downarrow_{\text{error}}$ and, for any w , $w \models (s, CM^\downarrow) \Downarrow_{\text{error}}$ (as $M \searrow_w M^\downarrow$). We therefore have:

$$\text{P}(w \models (s_w, CM^\downarrow) \Downarrow_{\text{error}}^u) \geq \text{P}(w \models (s_w, CM^\downarrow) \Downarrow_{\text{error}}) = 1 \geq \delta_1$$

(3) In the third case we have $(s, CM) \Downarrow_{\text{error}}^a$, for some $a \geq 0$ with $a \notin \text{Ran}(w_p)$. So, again arguing as above, but here using the second and third parts of the propositions, respectively, we have that $(s, C^\uparrow M) \Downarrow_{\text{error}}^a$, and that, for any w with $a \notin \text{Ran}(w)$, $w \models (s_w, CM^\downarrow) \Downarrow_{\text{error}}$. It follows that:

$$\text{P}(w \models (s_w, CM^\downarrow) \Downarrow_{\text{error}}^a) \geq \text{P}(w \#\{a\}) \geq \delta_1$$

(4) The fourth case is similar to the first case, but uses the third and fourth parts of the respective propositions.

□

Note the proof strategy used for the first half of the theorem: the behavior of a term CM of the instrumented high-level language is used to coordinate the behaviors of terms $C^\uparrow M$ and CM^\downarrow of the high- and low-level languages, respectively. The probability bound δ_1 arises because a non-public low-level memory access is made independently of the layout.

Using the evaluation function we obtain a weaker but more memorable statement:

COROLLARY 4.6. *Suppose that $M:\sigma$ is a high-level term and $C:\sigma \rightarrow \text{bool}$ is a public low-level term. Then, for any store s , we have:*

$$\text{P}(\text{Eval}(C^\uparrow M, s)_w = \text{Eval}_w(CM^\downarrow, s_w)) \geq \delta_1$$

Note that both Theorem 4.5 and Corollary 4.6 follow from the special case of the uniform distribution.

4.5 Equivalences

There is a natural relation of *public (contextual) operational (high-level) equivalence*, refining the standard relation of operational equivalence. It is defined by setting, for any two high-level terms, M, N of type σ :

$$M \approx_{h,p} N \iff \forall C:\sigma \rightarrow \text{bool}. CM \sim_{h,p} CN$$

where the quantification over C ranges over public high-level terms, and where, for high-level terms $M_0, N_0:\text{bool}$, we define:

$$M_0 \sim_{h,p} N_0 \iff \forall s. \text{Eval}(M_0, s) =_p \text{Eval}(N_0, s)$$

where $x =_p y$ holds if, and only if, either x and y have the forms (s, V) and (s', V') , and $s \upharpoonright \text{PubLoc} = s' \upharpoonright \text{PubLoc}$ and $V = V'$, or else $x = y = \text{error}$, or else $x = y = \Omega$. (As usual, if f is a function and S is a set then $f \upharpoonright S$ is the restriction of f to S .)

At low-level, for any low-level terms $M_0, N_0:\text{bool}$ say that $M_0 \sim_{l,p} N_0$ holds if, and only if, for every store s at least one of the following three possibilities holds:

- $\exists s', s'', V. \forall w. w \models (s_w, M_0) \implies (s'_w, V)$ and $w \models (s_w, N_0) \implies (s''_w, V)$ and $s' \upharpoonright \text{PubLoc} = s'' \upharpoonright \text{PubLoc}$,
- $\text{P}(w \models (s_w, M_0) \Downarrow_{\text{error}}^u) \geq \delta_1$ and $\text{P}(w \models (s_w, N_0) \Downarrow_{\text{error}}^u) \geq \delta_1$, or
- $\forall w. w \models (s_w, M_0) \Uparrow$ and $w \models (s_w, N_0) \Uparrow$.

This relation is a partial equivalence. (Reflexivity fails, in general, as a low-level term containing a secret location constant can branch on the address of that location in the memory.) If $\delta_1 > 0$ then the three possibilities are mutually exclusive; also, if the first of them holds, then s', s'' and V are uniquely determined. As may be expected, the relation is most restrictive in the case of the uniform distribution.

Now we define *public (contextual) operational (low-level) partial equivalence*, by putting, for low-level terms M, N of type σ :

$$M \approx_{l,p} N \iff \forall C:\sigma \rightarrow \text{bool}. CM \sim_{l,p} CN$$

where the C are restricted to be public low-level terms.

THEOREM 4.7. *Let $M, N : \sigma$ be high-level terms. Then, if σ is `loc`-free and $M \approx_{h,p} N$, then $M^\downarrow \approx_{l,p} N^\downarrow$. The converse holds for all σ , if $\delta_1 > 0$.*

PROOF. In one direction, we assume that σ is `loc`-free, so that $\sigma^\downarrow = \sigma$, and $M \approx_{h,p} N$, and then consider a public low-level term $C : \sigma \rightarrow \text{bool}$ in order to show $CM^\downarrow \sim_{l,p} CN^\downarrow$. Choose a store s . From the assumption that $M \approx_{h,p} N$ we then obtain $\text{Eval}(C^\uparrow M, s) =_p \text{Eval}(C^\uparrow N, s)$, and three cases arise.

In the first case we have that $(s, C^\uparrow M) \Rightarrow (s', V)$, $(s, C^\uparrow N) \Rightarrow (s'', V)$, and $s' \upharpoonright \text{PubLoc} = s'' \upharpoonright \text{PubLoc}$, for some s', s'' and V . Applying Theorem 4.5, we obtain that $w \models (s_w, CM^\downarrow) \Rightarrow (s'_w, V)$ and $w \models (s_w, CN^\downarrow) \Rightarrow (s''_w, V)$, for any w , which concludes this case.

In the second case we have $(s, C^\uparrow M) \Downarrow_{\text{error}}$ and $(s, C^\uparrow N) \Downarrow_{\text{error}}$. Then, by Theorem 4.5, $P(w \models (s_w, C^\uparrow M) \Downarrow_{\text{error}}^u) \geq \delta_1$ and $P(w \models (s_w, C^\uparrow N) \Downarrow_{\text{error}}^u) \geq \delta_1$. The third case is similar to the first two.

For the converse, we assume $M^\downarrow \approx_{l,p} N^\downarrow$ and consider a public high-level term $C : \sigma \rightarrow \text{bool}$ in order to show that $\text{Eval}(CM, s) =_p \text{Eval}(CN, s)$, for any given store s . We know that $C^\downarrow M^\downarrow \sim_{l,p} C^\downarrow N^\downarrow$, and also, by Proposition 4.4, that, for all w , $\text{Eval}(CM, s)_w = \text{Eval}_w(C^\downarrow M^\downarrow, s_w)$ and $\text{Eval}(CN, s)_w = \text{Eval}_w(C^\downarrow N^\downarrow, s_w)$.

The definition of $\sim_{l,p}$ then yields three cases, of which the first and third are immediate. For the second, as $\delta_1 > 0$, we have $w_1 \models (s_{w_1}, C^\downarrow M^\downarrow) \Downarrow_{\text{error}}^u$ and $w_2 \models (s_{w_2}, C^\downarrow N^\downarrow) \Downarrow_{\text{error}}^u$, for some w_1 and w_2 , and the conclusion follows. \square

5. THE RECOVERABLE-ERROR MODEL

Much like Section 4, this section defines a high-level language and a low-level language, and relates the security properties implied by the semantics of the former to those obtained from layout randomization in the latter. Here, however, unlike in Section 4, erroneous low-level memory accesses give rise to recoverable errors, and a local recovery mechanism is available for handling such errors. Moreover, there are no high-level errors.

Sections 5.1 and 5.2 present the high-level language and the low-level language, respectively. As in Section 4, we employ an instrumented high-level language in order to mediate between these two languages. This language, defined in Section 5.3, has facilities for memory access at both location and natural-number types, with an instrumented operational semantics that records natural-number memory accesses.

With these tools, we prove our main results for the recoverable-error model in Sections 5.4 and 5.5, to the effect that high- and low-level attackers have essentially equal power, modulo the translation from the high-level to the low-level language, and that this translation preserves and reflects suitable notions of contextual public equivalence. Thus, our main results are analogous to those for the fatal-error model. However, the recoverable-error model requires a more delicate analysis of probabilities, and also upper bounds on numbers of memory accesses.

5.1 The high-level language

The high-level language employs the abstract notion of location. The basic types are `nat` and `loc`, and the constants are the arithmetic constants, together with

constants for accessing and updating locations:

$$l_{\text{loc}} : \text{loc} \quad (l \in \text{Loc}) \quad !_{\text{loc}} : \text{loc} \rightarrow \text{nat} \quad :=_{\text{loc}} : \text{loc} \times \text{nat} \rightarrow \text{com}$$

Note that, unlike in the fatal-error case, there is no constant for raising an error. All the constants are values, and as well as the redexes specified by the general framework, there are the following two:

$$!_{\text{loc}} V \quad V :=_{\text{loc}} V$$

We define a *configuration* to be a pair (s, M) with s a store and M a well-typed term. The semantics of the high-level language then consists of a transition relation: $(s, M) \longrightarrow (s', M')$ which is obtained from the special case of redexes:

$$\frac{(s, R) \longrightarrow (s', M')}{(s, E[R]) \longrightarrow (s', E[M'])}$$

For redexes we take the transitions to be given by:

$$\begin{aligned} (s, !_{\text{loc}} l_{\text{loc}}) &\longrightarrow (s, n) \quad (s(l) = n) \\ (s, l_{\text{loc}} :=_{\text{loc}} n) &\longrightarrow (s[l \mapsto n], \text{skip}) \end{aligned}$$

and the rule:

$$\frac{R \longrightarrow M'}{(s, R) \longrightarrow (s, M')}$$

The big-step semantics is defined by:

$$\begin{aligned} (s, M) \Longrightarrow (s', V) &\iff (s, M) \rightarrow^* (s', V) \\ (s, M) \uparrow &\iff \forall n. \exists s', M'. (s, M) \rightarrow^n (s', M') \end{aligned}$$

The relation and property are mutually exclusive.

5.2 The low-level language

In the low-level language all memory accesses are made via natural numbers, just as in the fatal-error case. Consequently we take the only basic type to be nat . As well as the arithmetic constants, the low-level language has memory-access constants:

$$l_{\text{nat}} : \text{nat} \quad (l \in \text{Loc}) \quad !_{\text{nat}} : \text{nat} \rightarrow \text{nat}^e \quad :=_{\text{nat}} : \text{nat} \times \text{nat} \rightarrow \text{com}^e$$

where, for any type σ , we write σ^e for $\sigma + \text{unit}$. Note that there are constants for all the locations, not just the public ones. The type σ^e is used to model recoverable errors by values of the form $\text{inr } * : \sigma^e$, which we write as **error**. The cases mechanism for sum types makes it possible to write programs which deal with such errors. For instance, an attacker may run a program that explores a part of memory looking for an address that holds a natural number, such as the programs:

```
cases !nat 0 inl x : nat. x inr y : unit. 57
```

and

```
cases !nat 0 inl x : nat. x inr y : unit. cases !nat 1 inl z : nat. z inr u : unit. 57
```

which return the first natural number that they find in memory, and simply return the constant 57 in case of failure.

We take $!_{\text{nat}}$ and $:=_{\text{nat}}$ to be values, and l_{nat} to be a redex, for each $l \in \text{Loc}$. The other redexes are those specified by the general framework, together with:

$$!_{\text{nat}} V \quad V :=_{\text{nat}} V$$

Configurations in the low-level operational semantics are pairs (m, M) of a memory m and a well-typed term M . The semantics is defined relative to a choice of a memory layout. It consists of a transition relation, together with a family of transition relations parameterized by memory addresses $a \in \mathbb{N}$:

$$\begin{aligned} w \models (m, M) &\longrightarrow (m', M') \\ w \models (m, M) &\xrightarrow{a} (m', M') \quad (a \in \mathbb{N}) \end{aligned}$$

The family of transition relations is used to keep track of erroneous memory accesses. These are obtained from the special case of redexes in the usual way:

$$\begin{aligned} \frac{w \models (m, R) \longrightarrow (m', M')}{w \models (m, E[R]) \longrightarrow (m', E[M'])} \\ \frac{w \models (m, R) \xrightarrow{a} (m', M')}{w \models (m, E[R]) \xrightarrow{a} (m', E[M'])} \quad (a \in \mathbb{N}) \end{aligned}$$

For the redexes we take the transition relations to be given by the rule:

$$\frac{R \longrightarrow M'}{w \models (m, R) \longrightarrow (m, M')}$$

together with:

$$w \models (m, l_{\text{nat}}) \longrightarrow (m, w(l)) \quad (l \in \text{Loc})$$

and:

$$\begin{aligned} w \models (m, !_{\text{nat}} a) &\longrightarrow (m, \text{inl } n) \quad (\text{if } a \in \{0, \dots, c\} \text{ and } m(a) = n) \\ w \models (m, !_{\text{nat}} a) &\xrightarrow{a} (m, \text{error}) \quad (\text{if } a \notin \{0, \dots, c\} \text{ or } m(a) = *) \end{aligned}$$

and:

$$\begin{aligned} w \models (m, a :=_{\text{nat}} n) &\longrightarrow (m[a \mapsto n], \text{inl skip}) \quad (\text{if } a \in \{0, \dots, c\} \text{ and } m(a) \neq *) \\ w \models (m, a :=_{\text{nat}} n) &\xrightarrow{a} (m, \text{error}) \quad (\text{if } a \notin \{0, \dots, c\} \text{ or } m(a) = *) \end{aligned}$$

Notice that when an erroneous access is made then a non-fatal error arises.

For the low-level big-step semantics one needs to keep track of sets of erroneous memory accesses, not just single ones. Accordingly, for $A \subseteq \mathbb{N}$, define

$$w \models (m, M) \xrightarrow{A} (m', M')$$

to hold if either $A = \emptyset$ and $w \models (m, M) \longrightarrow (m', M')$, or else $A = \{a\}$ and $w \models (m, M) \xrightarrow{a} (m', M')$. Then define

$$w \models (m, M) \Longrightarrow (m', M')$$

to hold if there is a sequence:

$$(m, M) = (m_0, M_0), \dots, (m_n, M_n) = (m', M')$$

and sets $A_i \subseteq \mathbb{N}$, for $i = 1, n$, such that

$$w \models (m_{i-1}, M_{i-1}) \xrightarrow{A_i} (m_i, M_i)$$

for $i = 1, n$, and $A = \bigcup_{i=1}^n A_i$. Finally, define $(m, M) \uparrow^A$ to hold if there is an infinite sequence:

$$(m, M) = (m_0, M_0), \dots, (m_i, M_i), \dots$$

and sets $A_i \subseteq \mathbb{N}$, for $i \geq 1$, such that

$$w \models (m_{i-1}, M_{i-1}) \xrightarrow{A_i} (m_i, M_i)$$

for $i \geq 1$, and $A = \bigcup_{i=1}^{\infty} A_i$.

5.3 The instrumented high-level language

In order to relate the high-level semantics uniformly to the low-level language we instrument it by adding some constants for accessing the store at type **nat**. In the instrumented high-level language, accesses to the natural-number addresses of private locations will simply result in errors. In contrast, these accesses may work in the low-level language.

The instrumented high-level language has the same basic types as the high-level language and its constants are those of the high-level language together with:

$$l_{\text{nat}} : \text{nat} \quad (l \in \text{PubLoc}) \quad !_{\text{nat}} : \text{nat} \rightarrow \text{nat}^e \quad :=_{\text{nat}} : \text{nat} \times \text{nat} \rightarrow \text{com}^e$$

We take l_{nat} to be a redex (for $l \in \text{PubLoc}$), and $!_{\text{nat}}$ and $:=_{\text{nat}}$ to be values, and classify the other constants as in the case of the high-level language. The other redexes are those of the general framework, together with the following ones:

$$!_{\text{nat}} V \quad V :=_{\text{nat}} V \quad !_{\text{loc}} V \quad V :=_{\text{loc}} V$$

the latter two kinds being inherited from the high-level language.

For the operational semantics, configurations are defined as for the high-level language, but we add an instrumented transition relation:

$$(s, M) \xrightarrow{a} (s', M') \quad (a \in \mathbb{N})$$

We then proceed as for the high-level language, adding a rule for the instrumented transition relation:

$$\frac{(s, R) \xrightarrow{a} (s', M')}{(s, E[R]) \xrightarrow{a} (s', E[M'])}$$

together with:

$$(s, l_{\text{nat}}) \longrightarrow (s, w_p(l)) \quad (l \in \text{PubLoc})$$

and:

$$\begin{array}{ll} (s, !_{\text{nat}} a) \longrightarrow (s, \text{inl } s(l)) & (l \in \text{PubLoc}, a = w_p(l)) \\ (s, a :=_{\text{nat}} n) \longrightarrow (s[l \mapsto n], \text{inl skip}) & (l \in \text{PubLoc}, a = w_p(l)) \\ (s, !_{\text{nat}} a) \xrightarrow{a} (s, \text{error}) & (a \notin \text{Ran}(w_p)) \\ (s, a :=_{\text{nat}} n) \xrightarrow{a} (s, \text{error}) & (a \notin \text{Ran}(w_p)) \end{array}$$

For the big-step semantics one again needs to keep track of sets of non-public memory accesses. So define $(s, M) \xrightarrow{A} (s', M')$, where $A \subseteq \mathbb{N}$, to hold if either

$$A = \emptyset \text{ and } (s, M) \longrightarrow (s', M')$$

or else

$$A = \{a\} \text{ and } (s, M) \xrightarrow{a} (s', M')$$

Then define $(s, M) \xRightarrow{A} (s', M')$, where $A \subseteq \mathbb{N}$, to hold if there is a sequence:

$$(s, M) = (s_0, M_0) \xrightarrow{A_1} \dots \xrightarrow{A_n} (s_n, M_n) = (s', M')$$

with $n \geq 0$, such that $A = \bigcup_{i=1}^n A_i$ and define $(s, M) \uparrow^A$, where $A \subseteq \mathbb{N}$, to hold if there is an infinite sequence:

$$(s, M) = (s_0, M_0) \xrightarrow{A_1} \dots \xrightarrow{A_i} (s_i, M_i) \xrightarrow{A_{i+1}} \dots$$

such that $A = \bigcup_{i=1}^{\infty} A_i$.

Note that the small-step semantics of the instrumented high-level language is a conservative extension of that of the high-level language. That is, a transition $(s, M) \longrightarrow (s', M')$ holds in the high-level language if, and only if, it does in the instrumented high-level language. For the big-step semantics, we have, for any terms M, M' of the high-level language:

$$\begin{aligned} (s, M) \implies (s', M') &\iff (s, M) \xRightarrow{\emptyset} (s', M') \\ (s, M) \uparrow &\iff (s, M) \uparrow^{\emptyset} \end{aligned}$$

5.3.1 Translating instrumented high-level to high-level. Every term $M : \sigma$ of the instrumented high-level language can be translated to a term $M^\dagger : \sigma$ of the high-level language. First, as in the fatal-error case, we need a function to convert addresses of public locations to the locations themselves. Define the high-level term

$$G_\sigma : \text{nat} \rightarrow (\text{loc} \rightarrow \sigma) \rightarrow (\text{unit} \rightarrow \sigma) \rightarrow \sigma$$

to be:

$$\begin{aligned} \lambda x : \text{nat}. \lambda f : \text{loc} \rightarrow \sigma. \lambda g : \text{unit} \rightarrow \sigma. \\ \text{if } x = a_1 \text{ then } f((l^{(1)})_{\text{loc}}) \\ \text{elseif } x = a_2 \text{ then } f((l^{(2)})_{\text{loc}}) \\ \vdots \\ \text{elseif } x = a_p \text{ then } f((l^{(p)})_{\text{loc}}) \\ \text{else } g(*) \end{aligned}$$

where we make use of the enumeration of PubLoc and the definition of the a_i given in Section 4.3.1. **The term G_σ has as arguments a number x and two continuations; the first is used if x is the layout of a public location and the second otherwise.**

The translation replaces the additional constants as follows, leaving the others fixed:

$$\begin{aligned} l_{\text{nat}}^\dagger &= w_p(l) \quad (l \in \text{PubLoc}) \\ !_{\text{nat}}^\dagger &= \lambda x : \text{nat}. Gx(\lambda y : \text{loc}. \text{inl}(!_{\text{loc}}y))(\lambda y : \text{unit}. \text{error}) \\ :=_{\text{nat}}^\dagger &= \lambda x : \text{nat} \times \text{nat}. G(\text{fst } x)(\lambda y : \text{loc}. \text{inl}(y :=_{\text{loc}} \text{snd } x))(\lambda y : \text{unit}. \text{error}) \end{aligned}$$

The idea of the translation is to simulate non-public memory accesses by recoverable errors. It is correct in the following sense:

PROPOSITION 5.1. *Let M be a well-typed term of the instrumented high-level language. Then:*

- (1) *If M is a value then so is M^\uparrow .*
- (2) *If $(s, M) \xRightarrow{A} (s', V)$ then $(s, M^\uparrow) \Longrightarrow (s', V^\uparrow)$.*
- (3) *If $(s, M) \uparrow^A$ then $(s, M^\uparrow) \uparrow$.*

A small variation on this translation will also prove useful. For any $a \in \mathbb{N}$ define a translation M_a^\uparrow by the following alternative replacement of the additional constants.

$$\begin{aligned} (l_{\text{nat}})_a^\uparrow &= (l_{\text{nat}})^\uparrow \\ (!_{\text{nat}})_a^\uparrow &= \lambda x:\text{nat}. \text{if } x = a \text{ then } \Omega \text{ else } !_{\text{nat}}^\uparrow x \\ (:=_{\text{nat}})_a^\uparrow &= \lambda x:\text{nat} \times \text{nat}. \text{if fst } x = a \text{ then } \Omega \text{ else } :=_{\text{nat}}^\uparrow x \end{aligned}$$

PROPOSITION 5.2. *Let M be a well-typed term of the instrumented high-level language. Then, if $a \notin \text{Ran}(w_p)$:*

- (1) *If M is a value then so is M_a^\uparrow .*
- (2) *If $(s, M) \xRightarrow{A} (s', V)$ then $(s, M_a^\uparrow) \Longrightarrow (s', V_a^\uparrow)$, if $a \notin A$.*
- (3) *If $(s, M) \xRightarrow{A} (s', V)$ then $(s, M_a^\uparrow) \uparrow$, if $a \in A$.*
- (4) *If $(s, M) \uparrow^A$ then $(s, M_a^\uparrow) \uparrow$.*

5.3.2 *Translating instrumented high-level to low-level.* We can translate types σ and terms $M:\sigma$ of the instrumented high-level language to types σ^\downarrow and terms $M^\downarrow:\sigma^\downarrow$ of the low-level language. We obtain the translation σ^\downarrow of a type σ by replacing all occurrences of `loc` by `nat`. For terms we replace each occurrence of a type σ by one of σ^\downarrow and we replace the missing constants as follows:

$$\begin{aligned} (l_{\text{loc}})^\downarrow &= l_{\text{nat}} \\ (!_{\text{loc}})^\downarrow &= \lambda x:\text{nat}. \text{cases } !_{\text{nat}} x \text{ inl } y. y \text{ inr } z. 0 \\ (:=_{\text{loc}})^\downarrow &= \lambda x:\text{nat} \times \text{nat}. \text{cases } :=_{\text{nat}} x \text{ inl } y. y \text{ inr } z. \text{skip} \end{aligned}$$

and take the translation to act as the identity on the other constants, viz.: l_{nat} ($l \in \text{PubLoc}$), $!_{\text{nat}}$ and $:=_{\text{loc}}$.

The translation is correct with respect to the low-level semantics, in the sense, roughly, that M^\downarrow simulates M . As above we employ a simulation relation $M \searrow_w N$ between terms of the instrumented high-level language and terms of the low-level language, parameterized on a memory layout w .

We take this relation to be the least relation between terms of the instrumented high-level language and terms of the the low-level language which includes:

$$c \searrow_w c^\downarrow \quad l_{\text{loc}} \searrow_w w(l)$$

and which is closed under the other language constructs. For any term M of the instrumented high-level language we have $M \searrow_w M^\downarrow$; further, if $M:\sigma$ and $M \searrow_w N$ then $N:\sigma^\downarrow$.

We have a small-step simulation lemma:

LEMMA 5.3. *Suppose that $M \searrow_w N$ for well-typed terms M of the instrumented high-level language and N of the low-level language. Then:*

- (1) *If M is a value, then there is a V' , with $M \searrow_w V'$, such that, for any m , $w \models (m, N) \longrightarrow^* (m, V')$.*
- (2) *If $(s, M) \longrightarrow (s', M')$, then there is an N' , with $M' \searrow_w N'$, such that $w \models (s_w, N) \longrightarrow^* (s'_w, N')$.*
- (3) *If $(s, M) \xrightarrow{a} (s', M')$, and a is not in $\text{Ran}(w) \setminus \text{Ran}(w_p)$, then, for some N' such that $M' \searrow_w N'$, we have $w \models (s_w, N) \xrightarrow{a} (s'_w, N')$.*

The third case is particularly important as it enables one to find the memory access largely independently of the memory layout. In terms of big-step relations and properties we have:

PROPOSITION 5.4. *Suppose that $M \searrow_w N$ for well-typed terms M of the instrumented high-level language and N of the low-level language. Then:*

- (1) *If $(s, M) \xrightarrow{A} (s', V)$, then, if $w \# A$, there is a V' with $V \searrow_w V'$ such that $w \models (s_w, N) \xrightarrow{A} (s'_w, V')$.*
- (2) *If $(s, M) \uparrow^A$ then, if $w \# A$, $w \models (s_w, N) \uparrow^A$.*

5.4 High- and low-level attackers

We are now in a position to formulate our theorems for the recoverable-error case. Much as in the fatal-error case, we wish to show that a high-level term $M : \sigma$ is as secure as its low-level counterpart $M^\downarrow : \sigma^\downarrow$. We prove this [under the assumption that \$\sigma\$ is loc-free, i.e., that \$\sigma^\downarrow = \sigma\$](#) . (As in the fatal-error case, it does not hold more generally.)

Say that an instrumented high-level term (low-level term) is *public* if it contains no occurrence of any l_{loc} (respectively l_{nat}) with $l \in \text{PriLoc}$. We would again like to show that attackers gain no advantage by attacking at low-level rather than at high-level. As above, they certainly lose none since, for any public high-level term $C : \sigma \rightarrow \text{bool}$, the low-level term C^\downarrow is of equal attacking power. The following proposition, [which relates \$C\$ and \$C^\downarrow\$](#) , follows immediately from Proposition 5.4:

PROPOSITION 5.5. *Let $M : \sigma$ be a high-level term and let $C : \sigma \rightarrow \text{bool}$ be a public high-level term. Then:*

- (1) *If $(s, CM) \Longrightarrow (s', V)$ then, for any w , $w \models (s_w, C^\downarrow M^\downarrow) \xrightarrow{\emptyset} (s'_w, V)$.*
- (2) *If $(s, CM) \uparrow$ then, for any w , $w \models (s_w, C^\downarrow M^\downarrow) \uparrow^\emptyset$.*

These exhaust all the possibilities for the big-step semantics of CM .

[As above, we](#) restate this using evaluation functions. For any store s and term $M : \sigma$ of the instrumented high-level language (and so also any term of the high-level language) define their *behavior* $\text{Eval}(M, s)$ by:

$$\text{Eval}(M, s) = \begin{cases} (s', V) & \text{if } (s, M) \xrightarrow{A} (s', V) \\ \Omega & \text{if } (s, M) \uparrow^A \end{cases}$$

Note that we forget the A , regarding that as part of the instrumentation rather than the actual behavior. However, it also proves useful to define $\text{Acc}(M, s)$ to be $A \cap \{0, \dots, c\}$ when $(s, M) \xrightarrow{A} (s', V)$ or $(s, M) \uparrow^A$; $\text{Acc}(M, s)$ records the accesses made to non-public addresses.

Similarly, for any low-level term $M : \sigma$, memory m , and layout w define their *behavior* $\text{Eval}_w(M, m)$ by:

$$\text{Eval}_w(M, m) = \begin{cases} (m', V) & \text{if } w \models (m, M) \xrightarrow{A} (m', V) \\ \Omega & \text{if } w \models (m, M) \uparrow^A \end{cases}$$

It also proves useful to define $\text{Acc}_w(M, m)$ to be $(A \cap \{0, \dots, c\}) \setminus \text{Ran}(w_p)$ when $w \models (m, M) \xrightarrow{A} (m', V)$ or $w \models (m, M) \uparrow^A$; $|\text{Acc}_w(M, m)|$ measures the number of “relevant” memory accesses made by M , starting from m , meaning those erroneous accesses within memory bounds.

Analogously to Proposition 4.4, Corollary 5.6 expresses that for any public high-level term $C : \sigma \rightarrow \text{bool}$, the low-level term C^\downarrow is of equal attacking power, as stated above. We write x_w to mean (s_w, M) when x is (s, M) and Ω when x is Ω .

COROLLARY 5.6. *Let $M : \sigma$ be a high-level term and let $C : \sigma \rightarrow \text{bool}$ be a public high-level term. Then:*

$$\text{Eval}(CM, s)_w = \text{Eval}_w(C^\downarrow M^\downarrow, s_w)$$

for any s and w .

For a converse, suppose now that $C : \sigma \rightarrow \text{bool}$ is a public low-level term (so σ is **loc**-free). Then C is also a public instrumented high-level term of the same type, and we would like to show that the public high-level term $C^\uparrow : \sigma \rightarrow \text{bool}$ is an attacker of equal power. This will be true in a probabilistic sense.

The following theorem gives a lower bound on the probability that high- and low-level **behaviour** (for $C^\uparrow M$ and CM^\downarrow , respectively) coincide, where the layout w is allowed to vary according to its distribution and the store s is arbitrary. The theorem requires an bound on the number b of erroneous accesses: without that an attacker could explore all of memory. For small b , the high- and low-level semantics coincide the most, with probability close to 1 when c is sufficiently large and the probability distribution over the layouts is sufficiently uniform.

THEOREM 5.7. *Let $M : \sigma$ be a high-level term and let $C : \sigma \rightarrow \text{bool}$ be a public low-level term. Then, for any store s , and $0 \leq b \leq c - |\text{Loc}|$, one of the following holds:*

- (1) $\text{P}(|\text{Acc}_w(CM^\downarrow, s_w)| > b) \geq \delta_{b+1}$, or
- (2) $\text{P}(|\text{Acc}_w(CM^\downarrow, s_w)| \leq b \text{ and } \text{Eval}(C^\uparrow M, s)_w = \text{Eval}_w(CM^\downarrow, s_w)) \geq \delta_b$.

These alternatives are mutually exclusive if $\delta_{b+1} > 1/2$.

PROOF. Fix M , C , and s . The proof proceeds by cases on whether or not $|\text{Acc}(CM, s)| \leq b$. Suppose first that $|\text{Acc}(CM, s)| \leq b$. Take a w such that $w \# \text{Acc}(CM, s)$. Then, by Proposition 5.4, $\text{Acc}(CM, s) = \text{Acc}_w(CM^\downarrow, s_w)$ and $\text{Eval}(CM, s)_w = \text{Eval}_w(CM^\downarrow, s_w)$, as $CM \searrow_w (CM)^\downarrow = CM^\downarrow$. By Proposition 5.1 we also have that $\text{Eval}(C^\uparrow M, s) = \text{Eval}(CM, s)$.

We therefore have:

$$\begin{aligned} \delta_b &\leq \delta_{|\text{Acc}(CM, s)|} \\ &\leq \text{P}(w \# \text{Acc}(CM, s)) \\ &\leq \text{P}(|\text{Acc}_w(CM^\downarrow, s_w)| \leq b \text{ and } \text{Eval}(C^\uparrow M, s)_w = \text{Eval}_w(CM^\downarrow, s_w)) \end{aligned}$$

which is the second alternative.

Otherwise we have $|\text{Acc}(CM, s)| > b$. So, as $(s, CM) \xrightarrow{A} (s', M')$ for some s' , M' , and A with $\text{Acc}(CM, s) = A \cap \{0, \dots, c\}$, we have $(s, CM) \xrightarrow{A'} (s'', M'')$ for some s'' , M'' , and A' , where, setting $A'' = A' \cap \{0, \dots, c\}$, $|A''| = b + 1$.

Now, take a w such that $w \# A''$. Then, as $CM \searrow_w CM^\downarrow$, Lemma 5.3 implies that $w \models (s_w, CM^\downarrow) \xrightarrow{A'} (s''_w, N)$, for some N , and so $\text{Acc}_w(CM^\downarrow, s_w) \supseteq A''$ and $|\text{Acc}_w(CM^\downarrow, s_w)| > b$. We therefore have:

$$\begin{aligned} \delta_{b+1} &\leq \text{P}(w \# A'') \\ &\leq \text{P}(|\text{Acc}_w(CM^\downarrow, s_w)| > b) \end{aligned}$$

which is the first alternative. \square

We remark that, following its proof, the first of the alternatives of Theorem 5.7 holds if $|\text{Acc}(CM, s)| > b$ and the second if $|\text{Acc}(CM, s)| \leq b$. In the special case $b = 0$, the theorem implies that, for all s ,

- either $\text{P}(|\text{Acc}_w(CM^\downarrow, s_w)| > 0) \geq \delta_1$ or,
- for all w , $|\text{Acc}_w(CM^\downarrow, s_w)| = 0$ and $\text{Eval}(C^\uparrow M, s)_w = \text{Eval}_w(CM^\downarrow, s_w)$.

In other words, either an erroneous access to memory is probable, with probability at least δ_1 , or there is no such access and the high- and low-level semantics coincide. [Note too that the theorem follows from the special case of the uniform distribution.](#)

It is natural to wonder if the probability bound δ_{b+1} could be improved to δ_b in Theorem 5.7. The reason for the δ_{b+1} bound is that $|\text{Acc}_w(CM^\downarrow, s_w)|$ counts only erroneous accesses; what seems needed for a δ_b bound is a way of counting attacker guesses, including successful ones.

5.5 Equivalences

We define a relation of *(high-level) public (contextual) operational equivalence*, refining the standard relation of operational equivalence. For any two high-level terms, M, N of type σ , set:

$$M \approx_{h,p} N \iff \forall C : \sigma \rightarrow \text{bool}. CM \sim_{h,p} CN$$

where the quantification over C ranges over public high-level terms, and where, for high-level terms $M_0, N_0 : \text{bool}$, we define:

$$M_0 \sim_{h,p} N_0 \iff \forall s. \text{Eval}(M_0, s) =_p \text{Eval}(N_0, s)$$

where the relation $x =_p y$ holds if, and only if, either x and y have the forms (s, V) and (s', V') , and $s \upharpoonright \text{PubLoc} = s' \upharpoonright \text{PubLoc}$ and $V = V'$, or else $x = y = \Omega$.

In order to define a corresponding low-level relation, we first define a modified version of the low-level evaluation function that yields nontermination if there are

more than b erroneous accesses. For any $b \geq 0$, set:

$$\text{Eval}_w^b(M, m) = \begin{cases} \text{Eval}_w(M, m) & \text{if } |\text{Acc}_w(M, m)| \leq b \\ \Omega & \text{otherwise} \end{cases}$$

Next, for any b such that $0 \leq b \leq c - |\text{Loc}|$ and $\delta_{b+1} > 1/2$ we define a relation $\sim_{l,p}^b$ between low-level terms $M_0, N_0 : \text{bool}$, by taking $M_0 \sim_{l,p}^b N_0$ to hold if, and only if, for every store s one of the following (mutually exclusive) alternatives holds:

- for some $s' \upharpoonright \text{PubLoc} = s'' \upharpoonright \text{PubLoc}$ and V ,

$$\text{P}(\text{Eval}_w^b(M_0, s_w) = (s'_w, V)) \geq \delta_b \quad \text{and} \quad \text{P}(\text{Eval}_w^b(N_0, s_w) = (s''_w, V)) \geq \delta_b$$
- $\text{P}(\text{Eval}_w^b(M_0, s_w) = \Omega) \geq \delta_{b+1} \quad \text{and} \quad \text{P}(\text{Eval}_w^b(N_0, s_w) = \Omega) \geq \delta_{b+1}$

Note that we quantify over memories that are layouts of stores, not all memories. This relation is a partial equivalence: symmetry is evident, and transitivity follows from the assumption that $\delta_{b+1} > 1/2$, which ensures that the alternatives are mutually exclusive, and that, if the first alternative holds then s' , s'' and V are uniquely determined. (Reflexivity fails, in general, for the same reason as in the fatal-error case.) As may be expected, the relation is most restrictive in the case of the uniform distribution.

Now we define (*low-level*) *public (contextual) operational partial equivalence*, by setting, for any two low-level terms M, N of type σ :

$$M \approx_{l,p}^b N \iff \forall C : \sigma \rightarrow \text{bool}. CM \sim_{l,p}^b CN$$

where the contexts C are restricted to be public low-level terms.

The following theorem says, roughly, that two programs are publicly equivalent in the high-level language if, and only if, their translations are publicly equivalent in the low-level language, with the caveat that the low-level equivalence is probabilistic and conditioned on a bound b on the number of erroneous accesses.

THEOREM 5.8. *Let $M, N : \sigma$ be high-level terms. Then, assuming that σ is loc-free, $0 \leq b \leq c - |\text{Loc}|$, and $\delta_{b+1} > 1/2$, we have:*

$$M \approx_{h,p} N \quad \text{iff} \quad M^\downarrow \approx_{l,p}^b N^\downarrow$$

PROOF. In one direction, we assume that $M \approx_{h,p} N$, and then consider a public low-level term $C : \sigma \rightarrow \text{bool}$ in order to show that $CM^\downarrow \sim_{l,p}^b CN^\downarrow$. Choose a store s . Applying Theorem 5.7 to M and N , four cases arise.

(1) In the first case we have:

$$\text{P}(|\text{Acc}_w(CM^\downarrow, s_w)| > b) \geq \delta_{b+1} \quad \text{and} \quad \text{P}(|\text{Acc}_w(CN^\downarrow, s_w)| > b) \geq \delta_{b+1}$$

But then

$$\text{P}(\text{Eval}_w^b(CM^\downarrow, s_w) = \Omega) \geq \delta_{b+1} \quad \text{and} \quad \text{P}(\text{Eval}_w^b(CN^\downarrow, s_w) = \Omega) \geq \delta_{b+1}$$

concluding this case.

(2) In the second case we have:

$$\text{P} \left(\begin{array}{l} |\text{Acc}_w(CM^\downarrow, s_w)| \leq b \quad \text{and} \\ \text{Eval}(C^\uparrow M, s_w) = \text{Eval}_w(CM^\downarrow, s_w) \end{array} \right) \geq \delta_b$$

and

$$\mathbb{P} \left(\begin{array}{l} |\text{Acc}_w(CN^\downarrow, s_w)| \leq b \quad \text{and} \\ \text{Eval}(C^\uparrow N, s)_w = \text{Eval}_w(CN^\downarrow, s_w) \end{array} \right) \geq \delta_b$$

By assumption we have $\text{Eval}(C^\uparrow M, s) =_p \text{Eval}(C^\uparrow N, s)$ so there are two subcases.

(a) In the first, there are s' , s'' , and V such that $s' \upharpoonright \text{PubLoc} = s'' \upharpoonright \text{PubLoc}$, $\text{Eval}(C^\uparrow M, s) = (s', V)$, and $\text{Eval}(C^\uparrow N, s) = (s'', V)$. But then we have:

$$\mathbb{P}(\text{Eval}_w^b(CM^\downarrow, s_w) = (s'_w, V)) \geq \delta_b \quad \text{and} \quad \mathbb{P}(\text{Eval}_w^b(CN^\downarrow, s_w) = (s''_w, V)) \geq \delta_b$$

concluding this subcase.

(b) In the second, $\text{Eval}(C^\uparrow M, s) = \text{Eval}(C^\uparrow N, s) = \Omega$, and we obtain:

$$\mathbb{P}(\text{Eval}_w^b(CM^\downarrow, s_w) = \Omega) \geq \delta_b \quad \text{and} \quad \mathbb{P}(\text{Eval}_w^b(CN^\downarrow, s_w) = \Omega) \geq \delta_b$$

concluding this subcase.

(3) In the third case we have:

$$\mathbb{P}(|\text{Acc}_w(CM^\downarrow, s_w)| > b) \geq \delta_{b+1}$$

and

$$\mathbb{P} \left(\begin{array}{l} |\text{Acc}_w(CN^\downarrow, s_w)| \leq b \quad \text{and} \\ \text{Eval}(C^\uparrow N, s)_w = \text{Eval}_w(CN^\downarrow, s_w) \end{array} \right) \geq \delta_b$$

There are again two subcases.

(a) In the first, $\text{Eval}(C^\uparrow N, s)$ has the form (s', V) for some s' and V . So we have $(s, CN) \xrightarrow{A'} (s', V)$ for some A' with $A' \cap \{0, \dots, c\} = \text{Acc}(CN, s)$, as otherwise (i.e., if $(s, CN) \uparrow^{A''}$, for some A''), by Proposition 5.1 we would have a contradiction with the form of $\text{Eval}(C^\uparrow N, s)$.

By the remark after Theorem 5.7, and since the alternatives there are mutually exclusive, we have, using the assumptions of this case, that $|\text{Acc}(CM, s)| > b$; we similarly have that $|\text{Acc}(CN, s)| \leq b$. So $\text{Acc}(CM, s) \setminus \text{Acc}(CN, s)$ is non-empty, and we choose an element a of it; note that $a \notin \text{Ran}(w_p)$.

We know that $(s, CN) \xrightarrow{A'} (s', V)$. So, as $(CN)_a^\uparrow = C_a^\uparrow N$, by Proposition 5.2 we have $(s, C_a^\uparrow N) \Rightarrow (s', V)$. However, we have $(s, C_a^\uparrow M) \uparrow$ by parts 3 and 4 of Proposition 5.2. This contradicts the assumption that $M \approx_{h,p} N$.

(b) In the second, $\text{Eval}(C^\uparrow N, s) = \Omega$. But then we have

$$\mathbb{P}(\text{Eval}_w^b(CM^\downarrow, s_w) = \Omega) \geq \delta_{b+1} \quad \text{and} \quad \mathbb{P}(\text{Eval}_w^b(CN^\downarrow, s_w) = \Omega) \geq \delta_b \geq \delta_{b+1}$$

concluding this subcase.

(4) The fourth case is similar to the third.

In the other direction, assume that $M^\downarrow \approx_{l,p}^b N^\downarrow$ and then consider a public high-level term $C : \sigma \rightarrow \text{bool}$ in order to show, for a given store s , that $\text{Eval}(CM, s) =_p \text{Eval}(CN, s)$. We know that $C^\downarrow M^\downarrow \sim_{l,p}^b C^\downarrow N^\downarrow$. For any w we also know by Proposition 5.5 that $\text{Acc}_w(C^\downarrow M^\downarrow, s_w) = \emptyset$, so, by Corollary 5.6 and the definition of Eval_w^b , that

$$\text{Eval}(CM, s)_w = \text{Eval}_w(C^\downarrow M^\downarrow, s_w) = \text{Eval}_w^b(C^\downarrow M^\downarrow, s_w)$$

The same holds for N .

The definition of $\sim_{l,p}^b$ then yields two cases.

(1) In the first case we have:

$P(\text{Eval}_w^b(C^\downarrow M^\downarrow, s_w) = (s'_w, V)) \geq \delta_b$ and $P(\text{Eval}_w^b(C^\downarrow N^\downarrow, s_w) = (s''_w, V)) \geq \delta_b$
for some $s' | \text{PubLoc} = s'' | \text{PubLoc}$ and V . As $\delta_b > 0$, $\text{Eval}_{w'}^b(C^\downarrow M^\downarrow, s_{w'}) = (s'_{w'}, V)$
and $\text{Eval}_{w''}^b(C^\downarrow N^\downarrow, s_{w''}) = (s''_{w''}, V)$, for some w' and w'' . So,

$$\text{Eval}(CM, s)_{w'} = \text{Eval}_{w'}^b(C^\downarrow M^\downarrow, s_{w'}) = (s'_{w'}, V)$$

and

$$\text{Eval}(CN, s)_{w''} = \text{Eval}_{w''}^b(C^\downarrow N^\downarrow, s_{w''}) = (s''_{w''}, V)$$

As the map $s \mapsto s_w$ is injective, $\text{Eval}(CM, s) = (s', V)$ and $\text{Eval}(CN, s) = (s'', V)$.
Therefore, $\text{Eval}(CM, s) =_p \text{Eval}(CN, s)$, concluding this case.

(2) In the second case we have:

$$P(\text{Eval}_w^b(C^\downarrow M^\downarrow, s_w) = \Omega) \geq \delta_{b+1} \quad \text{and} \quad P(\text{Eval}_w^b(C^\downarrow N^\downarrow, s_w) = \Omega) \geq \delta_{b+1}$$

As $\delta_{b+1} > 0$ there are w' and w'' such that

$$\text{Eval}_{w'}(C^\downarrow M^\downarrow, s_{w'}) = \text{Eval}_{w''}(C^\downarrow N^\downarrow, s_{w''}) = \Omega$$

So $\text{Eval}(CM, s) = \text{Eval}(CN, s) = \Omega$ concluding the proof.

□

It would be interesting to look for stronger computational-soundness results. For example, one might consider changing $\sim_{i,p}^b$ so as not to conflate nontermination with too many erroneous accesses.

6. CONCLUSION

Given the abundance of disparate techniques for protection, it is useful to compare those techniques. Our results relate layout randomization to the static guarantees of the syntax of a high-level language in which the programs that represent attackers can neither mention private locations directly nor access them via natural-number addresses. Our work follows that of Pucella and Schneider [2006], which related obfuscation and type systems. However, their theorems do not explicitly mention resource bounds or probabilities, and focus on integrity properties. These theorems basically pertain to the protection—by obfuscation or typing—of a program from a potentially dangerous input. We consider more general attackers, represented by arbitrary contexts, and also treat program equivalences, capturing not only integrity but also secrecy properties. Despite these substantial differences, we share their goal of understanding randomization in the context of programming languages and their implementations.

Going further, one could study layout randomization for richer languages. Those languages may include richer type systems, concurrency, and dynamic allocation, in particular. For instance, they may allow the passing of locations (see Section 2), much like security protocols pass communication channels and cryptographic keys. Thus, despite the differences mentioned in the introduction, methods currently being developed in the study of security protocols could also be helpful in the study of layout randomization.

In another direction, one could explore variants and extensions—for instance, with replication (e.g., [Berger and Zorn 2006])— as well as other forms of randomization. For instance, our methods seem to apply to techniques that rename opcodes randomly. It may well also be possible to treat data structures or objects. To treat a fixed collection of arrays of given lengths, for example, one might proceed along the following lines. The memory model would specify the usual contiguous array layouts, and there would then be an analogue of the uniform distribution which assigns equal probabilities to all layouts. One would add a type `array` for arrays and facilities for handling them to the high-level languages. The low-level languages would remain as they are, except with the addition of constants of type `nat` for the arrays. Finally, the translations from the high-level to the low-level languages would amount to the usual implementation of the array operations, translating `array` to `nat`. We conjecture that analogues of all the above results would go through, except that it is not clear whether the uniform array layout distribution is optimal.

In such further advances, it may be tempting to develop and analyze sophisticated implementations that yield the strongest possible guarantees. Again, the analogy with security protocols may prove helpful. Nevertheless, those implementations would be of only limited interest unless they correspond to methods that could plausibly be used in actual systems. For instance, in models where the attacker may corrupt all shared memory (not common in security protocols), it may be futile to consider protection approaches that rely on frequent, extensive memory checks. Such difficulties should however encourage the development of programming models and constructs for which security guarantees can be realistically obtained. A promising step in this direction is the identification of the memory locations that are critical to security and require protection [Pattabiraman et al. 2008].

Acknowledgments

We are grateful to Adriana Compagnoni, Peter Druschel, Úlfar Erlingsson, Cédric Fournet, Sergio Maffei, Vaughan Pratt, Fred Schneider, and Ben Zorn for their questions, comments, and encouragement.

REFERENCES

- ABADI, M. 1998. Protection in programming-language translations. In *Proceedings of the 25th International Colloquium on Automata, Languages and Programming*, K. G. Larsen, S. Skyum, and G. Winskel, Eds. Lecture Notes in Computer Science, vol. 1443. Springer, 868–883.
- ABADI, M. 1999. Secrecy by typing in security protocols. *Journal of the ACM* 46, 5, 749–786.
- ABADI, M., BUDI, M., ERLINGSSON, U., AND LIGATTI, J. 2009. Control-flow integrity: principles, implementations, and applications. *ACM Transactions on Information and System Security* 13, 1, 1–40.
- ABADI, M. AND ROGAWAY, P. 2002. Reconciling two views of cryptography (The computational soundness of formal encryption). *Journal of Cryptology* 15, 2, 103–127.
- ANONYMOUS. 2002. Bypassing PaX ASLR protection. *Phrack* 11, 59.
- BACKES, M., HOFHEINZ, D., AND UNRUH, D. 2009. Cosp: a general framework for computational soundness proofs. In *16th ACM Conference on Computer and Communications Security*. 66–78.
- BARRANTES, E. G., ACKLEY, D. H., FORREST, S., AND STEFANOVIĆ, D. 2005. Randomized instruction set emulation. *ACM Transactions on Information and System Security* 8, 1, 3–40.
- BARTHE, G., REZK, T., AND BASU, A. 2007. Security types preserving compilation. *Computer Languages, Systems and Structures* 33, 2, 35–59.

- BERGER, E. D. AND ZORN, B. G. 2006. DieHard: probabilistic memory safety for unsafe languages. In *2006 ACM SIGPLAN Conference on Programming Language Design and Implementation*. 158–168.
- BHATKAR, S., DUVARNEY, D. C., AND SEKAR, R. 2003. Address obfuscation: an efficient approach to combat a broad range of memory error exploits. In *12th USENIX Security Symposium*.
- BHATKAR, S., SEKAR, R., AND DUVARNEY, D. C. 2005. Efficient techniques for comprehensive protection from memory error exploits. In *14th USENIX Security Symposium*.
- CHEN, S., JUN XU, E. C. S., GAURIAR, P., AND IYER, R. K. 2005. Non-control-data attacks are realistic threats. In *Proceedings of the Usenix Security Symposium*. 177–192.
- COMON-LUNDH, H. AND CORTIER, V. 2008. Computational soundness of observational equivalence. In *15th ACM Conference on Computer and Communications Security*. 109–118.
- DENNING, D. E. 1982. *Cryptography and Data Security*. Addison-Wesley, Reading, Mass.
- DRUSCHEL, P. AND PETERSON, L. L. 1992. High-performance cross-domain data transfer. Tech. Rep. TR 92-11, Department of Computer Science, The University of Arizona. Mar.
- ERLINGSSON, Ú. 2007. Low-level software security: Attacks and defenses. In *Foundations of Security Analysis and Design IV, FOSAD 2006/2007 Tutorial Lectures*, A. Aldini and R. Gorrieri, Eds. Lecture Notes in Computer Science, vol. 4677. Springer, 92–134.
- FELLEISEN, M. AND FRIEDMAN, D. P. 1986. Control operators, the secd-machine, and the lambda-calculus. In *3rd Working Conference on the Formal Description of Programming Concepts*. 193–219.
- FORREST, S., SOMAYAJI, A., AND ACKLEY, D. H. 1997. Building diverse computer systems. In *6th Workshop on Hot Topics in Operating Systems*. 67–72.
- FOURNET, C., GUERNIC, G. L., AND REZK, T. 2009. A security-preserving compiler for distributed programs: from information-flow policies to cryptographic mechanisms. In *16th ACM Conference on Computer and Communications Security*. 432–441.
- FOURNET, C. AND REZK, T. 2008. Cryptographically sound implementations for typed information-flow security. In *35th ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages*. 323–335.
- HASEGAWA, M. AND KAKUTANI, Y. 2002. Axioms for recursion in call-by-value. *Higher-Order and Symbolic Computation* 15, 2-3, 235–264.
- HOWARD, M. AND THOMLINSON, M. 2007. Windows Vista ISV security. <http://msdn2.microsoft.com/en-us/library/bb430720.aspx>.
- KC, G. S., KEROMYTIS, A. D., AND PREVELAKIS, V. 2003. Countering code-injection attacks with instruction-set randomization. In *10th ACM Conference on Computer and Communications security*. 272–280.
- KIRIANSKY, V., BRUENING, D., AND AMARASINGHE, S. 2002. Secure execution via program shepherding. In *11th USENIX Security Symposium*. 191–206.
- MEDEL, R. H. 2006. Typed assembly languages for software security. Ph.D. thesis, Stevens Institute of Technology.
- MITCHELL, J. 1996. *Foundations for Programming Languages*. MIT Press.
- MOGGI, E. 1989. Computational lambda-calculus and monads. In *Fourth Annual IEEE Symposium on Logic in Computer Science*. 14–23.
- MOGGI, E. 1991. Notions of computation and monads. *Information and Computation* 93, 1, 55–92.
- MORRIS, JR., J. H. 1973. Protection in programming languages. *Communications of the ACM* 16, 1, 15–21.
- MORRISETT, G., WALKER, D., CRARY, K., AND GLEW, N. 1999. From System F to typed assembly language. *ACM Transactions on Programming Languages and Systems* 21, 3, 527–568.
- NOVARK, G. AND BERGER, E. D. 2010. DieHarder: securing the heap. In *17th ACM Conference on Computer and Communications security*. 573–584.
- NOVARK, G., BERGER, E. D., AND ZORN, B. G. 2008. Exterminator: Automatically correcting memory errors with high probability. *Communications of the ACM* 51, 12, 87–95.

- PATTABIRAMAN, K., GROVER, V., AND ZORN, B. G. 2008. Samurai: protecting critical data in unsafe languages. In *EuroSys*. 219–232.
- PAX PROJECT. 2004. The PaX project. <http://pax.grsecurity.net/>.
- PIERCE, B. 2002. *Types and Programming Languages*. MIT Press.
- PUCELLA, R. AND SCHNEIDER, F. B. 2006. Independence from obfuscation: A semantic framework for diversity. In *19th IEEE Computer Security Foundations Workshop*. 230–241.
- SHACHAM, H., PAGE, M., PFAFF, B., GOH, E.-J., MODADUGU, N., AND BONEH, D. 2004. On the effectiveness of address-space randomization. In *11th ACM Conference on Computer and Communications Security*. 298–307.
- SOTIROV, A. AND DOWD, M. 2008. Bypassing browser memory protections: Setting back browser security by 10 years. <http://taossa.com/archive/bh08sotirovdowd.pdf>.
- SOVAREL, A. N., EVANS, D., AND PAUL, N. 2005. Where’s the FEED? the effectiveness of instruction set randomization. In *14th USENIX Security Symposium*. 145–160.
- VOLPANO, D., IRVINE, C., AND SMITH, G. 1996. A sound type system for secure flow analysis. *Journal of Computer Security* 4, 167–187.
- YARVIN, C., BUKOWSKI, R., AND ANDERSON, T. 1993. Anonymous RPC: Low-latency protection in a 64-bit address space. In *USENIX Summer Technical Conference*. 175–186.

APPENDIX

We present some technical material, mainly preliminary results needed for the proofs of the main theorems.

A. THE FATAL-ERROR MODEL

A.1 The high-level language

There is a small-step subject-reduction theorem for the high-level semantics:

LEMMA A.1. *For any configuration (s, M) , with $M:\sigma$, one of the following three mutually exclusive statements holds:*

- M is a value,
- $(s, M) \longrightarrow (s', M')$ for some uniquely determined s' and $M':\sigma$, or
- $(s, M) \Downarrow_{\text{error}}$.

This lemma yields a big-step subject-reduction theorem:

LEMMA A.2. *For any configuration (s, M) , with $M:\sigma$, one of the following three mutually exclusive statements holds:*

- $(s, M) \Longrightarrow (s', V)$ for a unique s' and $V:\sigma$,
- $(s, M) \Downarrow_{\text{error}}$, or
- $(s, M) \Uparrow$.

A.2 The low-level language

The small-step subject-reduction theorem for the low-level language is as follows:

LEMMA A.3. *For any memory layout w and configuration (m, M) , with $M:\sigma$, one of the following four mutually exclusive statements holds:*

- M is a value,
- $w \models (m, M) \longrightarrow (m', M')$ for some uniquely determined m' and $M':\sigma$, and if m has the form s_w , so does m' ,

- $w \models (m, M) \downarrow_{\text{error}}$, or
- $w \models (m, M) \downarrow_{\text{error}}^a$ for some uniquely determined a .

The corresponding big-step subject-reduction theorem is:

LEMMA A.4. *For any memory layout w and configuration (m, M) , with $M : \sigma$, one of the following four mutually exclusive statements holds:*

- $w \models (m, M) \implies (m', V)$ for a unique m' and $V : \sigma$, and if m has the form s_w , so does m' ,
- $w \models (m, M) \downarrow_{\text{error}}$,
- $w \models (m, M) \downarrow_{\text{error}}^a$, for some uniquely determined a , or
- $w \models (m, M) \uparrow$.

A.3 The instrumented high-level language

For the analogue to Lemma A.1, one adds one more possibility to the list of mutually exclusive possibilities:

- $(s, M) \downarrow_{\text{error}}^a$ for some uniquely determined a .

and, for the analogue of Lemma A.2 one adds the following possibility to the list of mutually exclusive possibilities:

- $(s, M) \downarrow_{\text{error}}^a$ for some unique a .

A.3.1 *Translating instrumented high-level to high-level.* The translation is correct for the small-step semantics, in the following sense (where $(s, M) \downarrow_{\text{error}}^u$ holds if, and only if, either $(s, M) \downarrow_{\text{error}}$ or $(s, M) \downarrow_{\text{error}}^a$, for some a):

LEMMA A.5. *Let M be a well-typed term of the instrumented high-level language. Then:*

- (1) *If M is a value then so is M^\dagger .*
- (2) *If $(s, M) \longrightarrow (s', M')$ then $(s, M^\dagger) \longrightarrow^* (s', (M')^\dagger)$.*
- (3) *If $(s, M) \downarrow_{\text{error}}^u$ then $(s, M^\dagger) \longrightarrow^* (s, M') \downarrow_{\text{error}}$, for some M' .*

PROOF. Part 1 follows by inspection. For part 2, one shows first that, for any redex R , if $(s, R) \longrightarrow (s', M')$ then $(s, R^\dagger) \longrightarrow^* (s', (M')^\dagger)$. One shows next that, if E is an evaluation context, then E^\dagger is too (taking $[-]^\dagger = [-]$, etc.) and $E[M]^\dagger = E^\dagger[M^\dagger]$. Part 2 then follows. For part 3, one shows first that, for any redex R , if $(s, R) \downarrow_{\text{error}}^u$ then $(s, R^\dagger) \longrightarrow^* (s, M') \downarrow_{\text{error}}$, for some M' , and then applies the previous remark on evaluation contexts. \square

Proposition 4.1, the corresponding translation-correctness result for the big-step semantics, is an immediate consequence of this lemma.

A.3.2 *Translating instrumented high-level to low-level.* A series of lemmas leads to the main simulation results. The first lemma concerns values.

LEMMA A.6. *Suppose that $V \searrow_w N$ for a well-typed value V . Then for some V' with $V \searrow_w V'$ we have, for any m , $w \models (m, N) \longrightarrow^* (m, V')$.*

The second lemma concerns redexes.

LEMMA A.7. *Suppose that $R \searrow_w N$ and that $(s, R) \longrightarrow (s', M')$. Then for some N' with $M' \searrow_w N'$ we have $w \models (s_w, N) \longrightarrow^* (s'_w, N')$.*

PROOF. The proof is straightforward. The case where $R \longrightarrow M'$ makes use of Lemma A.6. For example, suppose that R is $\mathbf{fst}(V, V')$. Then N has the form $\mathbf{fst}(N_1, N'_1)$ where $V \searrow_w N_1$ and $V' \searrow_w N'_1$. Applying Lemma A.6 twice, we then have that:

$$(s_w, \mathbf{fst}(N_1, N'_1)) \longrightarrow (s_w, \mathbf{fst}(V_1, V'_1)) \longrightarrow (s_w, V_1)$$

for some values V_1, V'_1 with $V \searrow_w V_1$ and $V' \searrow_w V'_1$. \square

The third lemma concerns evaluation contexts. The simulation relation is extended in an evident way to evaluation contexts, taking, $[-] \searrow_w [-]$, etc. One easily sees that if $E \searrow_w E'$ and $M \searrow_w N$ then $E[M] \searrow_w E'[N]$.

LEMMA A.8. *Suppose that $E[R] \searrow_w N$. Then N has the form $E'[N_1]$ where $E \searrow_w E'$ and $R \searrow_w N_1$.*

We now have a small-step simulation lemma:

LEMMA A.9. *Suppose that $M \searrow_w N$ for a well-typed terms M of the instrumented high-level language and N of the low-level language. Then:*

- (1) *If M is a value, then there is a V' , with $M \searrow_w V'$, such that for any m , $w \models (m, N) \longrightarrow^* (m, V')$.*
- (2) *If $(s, M) \longrightarrow (s', M')$, then there is an N' , with $M' \searrow_w N'$, such that $w \models (s_w, N) \longrightarrow^* (s'_w, N')$.*
- (3) *If $(s, M) \downarrow_{\text{error}}$ then $w \models (s_w, N) \downarrow_{\text{error}}$.*
- (4) *If $(s, M) \downarrow_{\text{error}}^a$ then, if $a \notin \text{Ran}(w)$, $w \models (s_w, N) \downarrow_{\text{error}}^a$.*

PROOF. The first part is an immediate consequence of Lemma A.6, as is the second part of Lemmas A.7 and A.8. For the third and fourth parts, one verifies the assertions when M is a redex, and then applies Lemma A.8. \square

Proposition 4.2, the corresponding big-step simulation result, is an immediate consequence of this lemma.

B. THE RECOVERABLE-ERROR MODEL

B.1 The high-level language

The small-step subject-reduction theorem is:

LEMMA B.1. *For any configuration (s, M) , with $M:\sigma$, one of the following two mutually exclusive statements holds:*

- M is a value, or
- $(s, M) \longrightarrow (s', M')$ for some uniquely determined s' and $M':\sigma$.

and the big-step subject-reduction theorem is:

LEMMA B.2. *For any configuration (s, M) , with $M:\sigma$, one of the following two mutually exclusive statements holds:*

- $(s, M) \Longrightarrow (s', V)$ for a unique s' and $V:\sigma$, or
- $(s, M) \uparrow$.

B.2 The low-level language

Here the small-step subject-reduction theorem is:

LEMMA B.3. *For any memory layout w and configuration (m, M) , with $M : \sigma$, one of the following three mutually exclusive statements holds:*

- M is a value,
- $w \models (m, M) \longrightarrow (m', M')$ for some uniquely determined m' and $M' : \sigma$, and if m has the form s_w , so does m' , or
- $w \models (m, M) \xrightarrow{a} (m', M')$ for some uniquely determined a , m' , and $M' : \sigma$, and if m has the form s_w , so does m' .

and the big-step subject-reduction theorem is:

LEMMA B.4. *For any configuration (m, M) , with $M : \sigma$, one of the following two mutually exclusive statements holds:*

- $w \models (m, M) \xRightarrow{A} (m', V)$ for a unique m' , $V : \sigma$, and $A \subseteq \mathbb{N}$, and if m has the form s_w , so does m' , or
- $w \models (m, M) \uparrow^A$ for a unique $A \subseteq \mathbb{N}$.

B.3 The instrumented high-level language

For the analogue to Lemma B.1, one adds one more possibility to the list of mutually exclusive possibilities:

- $(s, M) \xrightarrow{a} (s', M')$ for some unique a , s' , and M' .

The big-step subject-reduction theorem is then:

LEMMA B.5. *For any configuration (s, M) , with $M : \sigma$, one of the following two mutually exclusive statements holds:*

- $(s, M) \xRightarrow{A} (s', V)$ for a unique s' , $V : \sigma$, and $A \subseteq \mathbb{N}$, or
- $(s, M) \uparrow^A$ for a unique $A \subseteq \mathbb{N}$.

B.3.1 *Translating instrumented high-level to high-level.* Proposition 5.1 follows from the following small-step translation correctness result:

LEMMA B.6. *Let M be a well-typed term of the instrumented high-level language. Then:*

- (1) *If M is a value then so is M^\uparrow .*
- (2) *If $(s, M) \xrightarrow{A} (s', M')$ then $(s, M^\uparrow) \longrightarrow^* (s', (M')^\uparrow)$.*

PROOF. Part 1 follows by inspection. For part 2, one shows first that, for any redex R , if $(s, R) \xrightarrow{A} (s', M')$ then $(s, R^\uparrow) \longrightarrow^* (s', (M')^\uparrow)$. One shows next that if E is an evaluation context, then E^\uparrow is too (taking $[-]^\uparrow = [-]$, etc.) and that $E[M]^\uparrow = E^\uparrow[M^\uparrow]$. Part 2 then follows. \square

B.3.2 *Translating instrumented high-level to low-level.* As above, a series of lemmas leads to the main simulation results.

LEMMA B.7. *Suppose that $V \searrow_w N$ for a well-typed value V . Then for some V' with $V \searrow_w V'$ we have, for any m , $w \models (m, N) \longrightarrow^* (m, V')$.*

The second lemma concerns redexes.

LEMMA B.8. (1) *Suppose that $R \searrow_w N$ and that $(s, R) \longrightarrow (s', M')$. Then we have $w \models (s_w, N) \longrightarrow^* (s'_w, N')$ for some N' with $M' \searrow_w N'$.*

(2) *Suppose that $R \searrow_w N$ and that $(s, R) \xrightarrow{a} (s', M')$ for some a not in $\text{Ran}(w) \setminus \text{Ran}(w_p)$. Then we have $w \models (s_w, N) \xrightarrow{a} (s'_w, N')$ for some N' with $M' \searrow_w N'$.*

The third lemma concerns evaluation contexts. The simulation relation is again extended in the evident way to evaluation contexts, taking $[-] \searrow_w [-]$, etc. One easily sees that if $E \searrow_w E'$ and $M \searrow_w N$ then $E[M] \searrow_w E'[N]$.

LEMMA B.9. *Suppose that $E[R] \searrow_w N$. Then N has the form $E'[N_1]$ where $E \searrow_w E'$ and $R \searrow_w N_1$.*

Using these results, one obtains a small-step simulation lemma, Lemma 5.3, which, in its turn, yields Proposition 5.4, the big-step simulation result.