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# 10-Gb/s Transmission Over 10-m SI-POF With $M$ -PAM and Multilayer Perceptron Equalizer

Isaac N. Osahon<sup>1b</sup>, Majid Safari, and Wasiiu O. Popoola<sup>1b</sup>

**Abstract**—We demonstrate the gigabit-per-second transmission over a step-index plastic optical fiber (SI-POF) of 10-m length with a pulse-amplitude modulation (PAM). A multilayer perceptron-based equalizer is used to mitigate an intersymbol interference and non-linearity in the system. Using this equalizer with 32-PAM, a data rate of 10 Gb/s is achieved over the 10-m SI-POF at a bit error rate of  $10^{-2}$ , which is below the 20% forward error correction limit.

**Index Terms**—Polymer optical fiber (POF), multilayer perceptron (MLP), decision feedback equalizer, pulse amplitude modulation (PAM), non-linearity.

## I. INTRODUCTION

POLYMER optical fiber (POF) continues to gain prominence as a promising inexpensive medium for indoor networks due to its ease of installation and handling, resistance to electromagnetic interference (EMI) and its lower weight [1]. However, these advantages come at the cost of limited bandwidth-length product ( $45 \text{ MHz} \times 100 \text{ m}$ ) and high attenuation ( $0.15 \text{ dB/m}$  at  $650 \text{ nm}$  wavelength) particularly for step index POF (SI-POF) [2].

A viable solution for the SI-POF limited bandwidth problem is to use multilevel pulse amplitude modulation ( $M$ -PAM) scheme with equalization techniques. With the availability of high power optical sources and sensitive linear receivers, multilevel signalling  $\{M \in 2, 4, 8, 16, 32\}$  with the conventional decision feedback equalizer (DFE) has been used to achieve data transmission up to 5 Gbps via SI-POF of less than 20 m length [3], [4]. Another set of equalizers used in digital communication systems is the neural network (NN) equalizers and one major NN architecture is the multilayer perceptron (MLP). A comprehensive detail on MLP can be explored in [5]. A 3 layer perceptron based-DFE is illustrated in Fig. 1 with the sigmoid function used for the hidden layer neurons and the linear function used for the output layer neuron. A key advantage of MLP over the conventional equalizer is that it does not only compensate for intersymbol interference (ISI) but also non-linearities in the system [6]. Moreover, it has been shown from a simulation study that under severe ISI, MLP offers better bit-error rate (BER)

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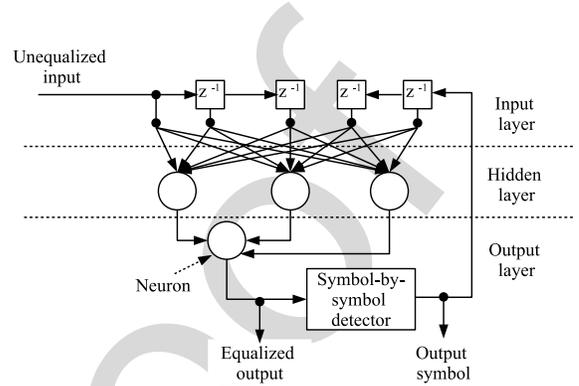


Fig. 1. Decision feedback equalizer structure with three forward taps and two feedback taps for MLP with three hidden layer neurons.

performance than the conventional equalizer for a Gaussian fitted theoretical SI-POF channel [7].

This letter therefore experiments  $M$ -PAM transmission over 10 m SI-POF with the MLP based DFE (MLP-DFE). In the experiment, a red laser diode (LD, L650P007, 650 nm wavelength, numerical aperture  $\approx 0.15$ , 7 mW optical power) is used as the optical source. The receiver is the New Focus Model 1601 with a responsivity of 0.45 A/W and a noise equivalent power of  $35 \text{ pW/Hz}^2$  at 650 nm wavelength. The conventional transversal DFE (TR-DFE) is also considered in this experiment for comparison purpose. The recursive least squares (RLS) algorithm with forgetting factor of one is used to train the TR-DFE while the Levenberg-Marquardt back propagation (LMBP) algorithm is used for the MLP-DFE.

## II. THE EXPERIMENTAL SET-UP

A block diagram illustrating the experimental set-up is shown in Fig. 2. It should be noted that the technique for coupling both the LD and receiver to the SI-POF is butt coupling [2], [8]. A simplified expression for the link's output signal  $I_{rp}(t)$  is provided as:

$$I_{rp}(t) = \mathcal{R}_p \alpha_p (P_{opt}(t) \otimes h_{pch}(t)) + n_p(t), \quad (1)$$

where  $\mathcal{R}_p$  denotes the photodiode (PD) responsivity in A/W;  $\alpha_p$  denotes the attenuation of the POF channel;  $P_{opt}(t)$  denotes the optical signal from the LD;  $h_{pch}(t)$  is the channel's impulse response; and  $n_p(t)$  is the noise at the receiver. The end-to-end frequency response of the link is shown in Fig. 3, indicating a 3 dB bandwidth of  $\sim 180 \text{ MHz}$ .

The transmitted binary data is a uniformly distributed random data with a length of  $10^6$  bits. The binary data is  $M$ -PAM

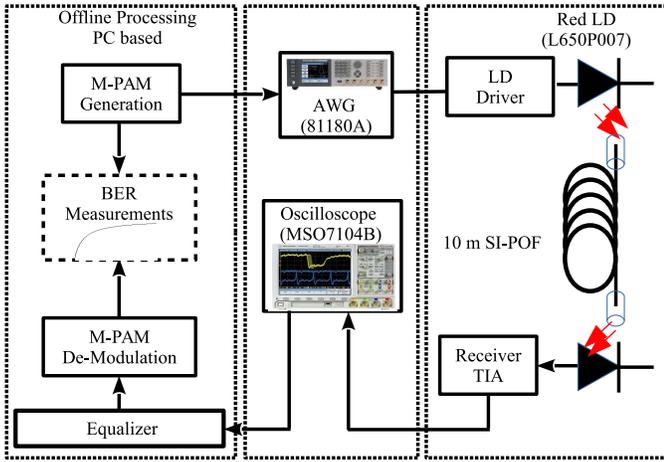


Fig. 2. Block diagram illustration for the Experiment.

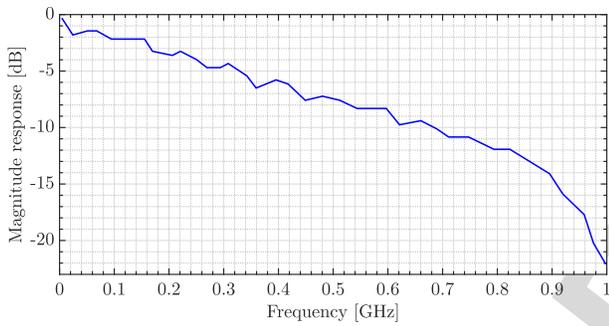


Fig. 3. Measured frequency response of the POF system.

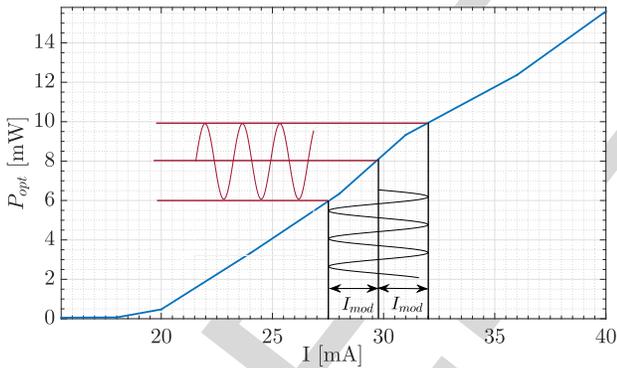


Fig. 4. Measured P-I curve of L650P007.

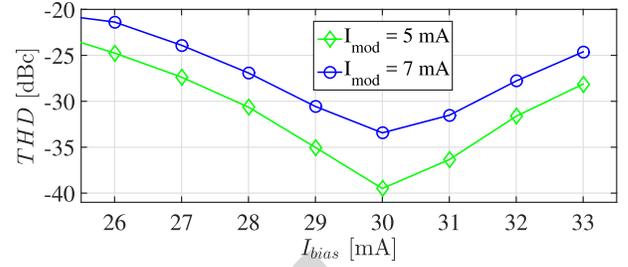


Fig. 5. LD's total harmonic distortion relative to  $I_{bias}$ .

total harmonic distortion ( $THD$ ). Fig. 5 shows the measured  $THD$  for the LD at different bias current ( $I_{bias}$ ) and under peak modulating current ( $I_{mod}$ ) of 5 mA and 7 mA. The  $THD$  in dBc is defined as:

$$THD = 10 \log_{10} \left( \frac{1}{P_1} \sum_{n=2}^{\infty} P_n \right), \quad (2)$$

where  $P_n$  is the power (in Watts) for the  $n^{\text{th}}$  harmonic with  $n = 1$  being the fundamental frequency. The  $THD$  is obtained by measuring the power of the first five harmonics using a fundamental frequency of 40 MHz. The plots in Fig. 5 suggest that a bias current of 30 mA should be used for minimal non-linearity from the LD. Fig. 5 particularly shows that increasing  $I_{mod}$  increases the non-linearity of the LD, as the  $THD$  is about  $-33$  dBc and  $-39$  dBc when  $I_{mod}$  is 7 mA and 5 mA respectively. The LD is therefore biased at about 30 mA with  $I_{mod} = 6.5$  mA resulting to an optical modulation index ( $\eta_{mod}$ ) of 0.65, where  $\eta_{mod} = \frac{I_{mod}}{I_{bias} - I_{th}}$ . The LD's output is then transmitted through a 10 m SI-POF (HFBR-RUD500Z, core diameter = 1 mm) with a measured attenuation of  $\sim 1.8$  dB at DC. The received optical signal from the SI-POF is converted into an electrical signal with the photo-receiver and the resulting signal is captured with an oscilloscope (MSO7104B). The captured signal is then imported into the PC for post-processing, which comprises of filtering, down-sampling and equalization. Finally, the equalized output is demodulated, thus offering the received binary data, that in turn is used to evaluate the bit-error-rate (BER).

### III. EQUALIZER PARAMETERS

The number of input taps and training examples are important parameters required for a DFE. The optimum number of taps for a DFE depends on the data rate and the PAM level. But for fair comparison under similar conditions, both DFEs have 22 forward taps and 18 feedback taps. To determine the optimum number of training examples for the DFEs, the BER is obtained for different training examples as shown in Fig. 6. The plots in Fig. 6 shows that while 2000 symbols are enough for the transversal DFE, the MLP-DFE requires at least 3000. Thus, 4000 training symbols are selected for both DFEs.

The number of hidden layer neurons is another parameter for MLP-DFE. For this study, it is chosen as six because there is no proper definition for optimum value to the best of the authors' knowledge.

modulated by mapping  $\log_2(M)$  bits to one of  $M$  amplitude levels with gray coding. The data symbols are preceded by a preamble of 4000 symbols that is used for synchronization at the receiver and for training the equalizer. The resulting symbol sequence is upsampled and fed through a digital pulse shaping filter, which for this work, is a root-raised-cosine filter with roll-off factor of 0.5. The modulated and pulse shaped signal is then loaded to an arbitrary waveform generator (AWG, Keysight 81180A).

The measured P-I curve of the LD is depicted in Fig. 4 and it shows the threshold current ( $I_{th}$ ) of the LD to be 20 mA. The non-linearity of the LD is quantified by measuring its

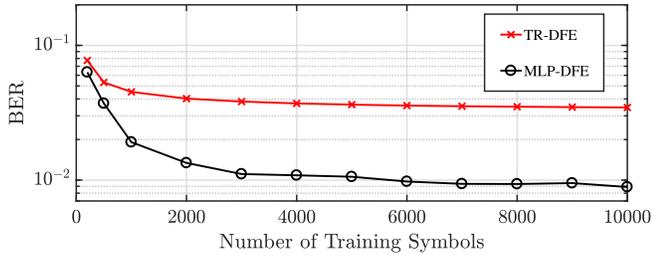


Fig. 6. BER plot comparing the equalizers' performance for different training symbols at a data rate of 10 Gbps with 32-PAM.

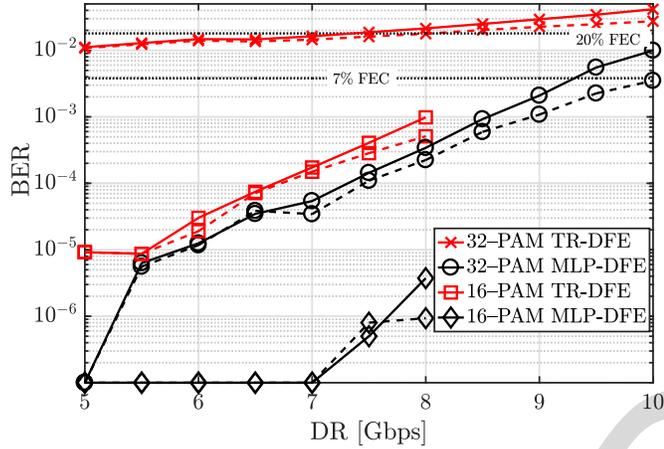


Fig. 7. BER plots comparing the MLP based DFE with the conventional DFE. The solid and dashed lines represents the BER if the feedback inputs for the DFEs are the detected and transmitted symbols respectively.

#### IV. RESULTS AND DISCUSSIONS

123 The maximum data rate at a given value of  $M$  is limited  
 124 to  $2 \log_2(M)$  Gbps, because the maximum sampling rate of  
 125 the AWG is 4 Gsa/s (from Nyquist theory). So, for  $M = 4$   
 126 and  $M = 8$ , the maximum data rate is limited to 4 Gbps  
 127 and 6 Gbps respectively. Therefore, 16-PAM and 32-PAM are  
 128 considered owing to their higher spectral efficiency and the  
 129 sufficient received SNR for the 10 m SI-POF. The BER plot  
 130 for 16-PAM and 32-PAM is shown in Fig. 7. With 16-PAM,  
 131 8 Gbps is achieved at a BER of  $10^{-3}$  with the TR-DFE.  
 132 With the MLP-DFE however, the BER is  $\sim 3.5 \times 10^{-6}$   
 133 at 8 Gbps data rate. For 32-PAM, the BER at 5.5 Gbps is  
 134 about  $10^{-2}$  using the TR-DFE, but this is below  $10^{-5}$  with the  
 135 MLP equalizer. A gross data rate of 10 Gbps is achieved with  
 136 32-PAM and the MLP equalizer at a BER of  $10^{-2}$  which  
 137 is below the 20% forward error correction (FEC) limit as  
 138 Table I shows. Furthermore, the eye diagram in Fig. 8 clearly  
 139 shows a wider eye opening with the MLP-DFE compared to  
 140 the conventional DFE for 5.5 Gbps using both 16-PAM and  
 141 32-PAM.

142 A drawback of the DFE configuration is their susceptibility  
 143 to error propagation due to occasional decision error of their  
 144 input feedback symbols. To assess this error propagation effect  
 145 on the conventional DFE and MLP-DFE, the BER result is also  
 146 computed using the transmitted symbols as feedback data. This  
 147 way, the feedback signal is always error free and therefore

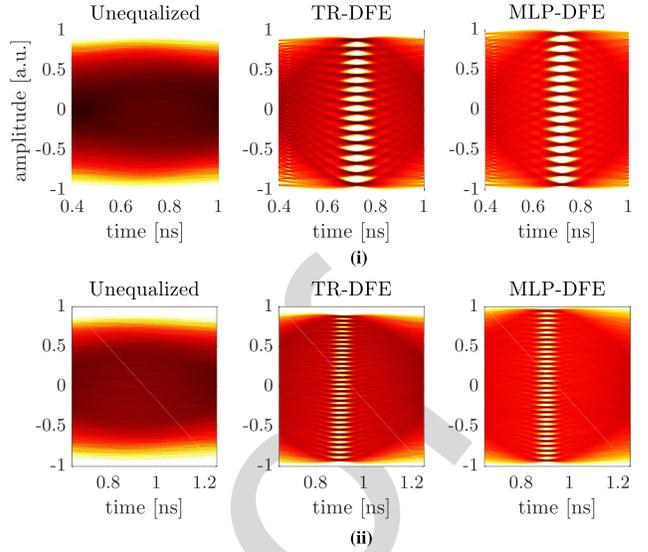


Fig. 8. Eye diagram comparing the equalizers' performance for 5.5 Gbps with: (i) 16-PAM (ii) 32-PAM.

TABLE I  
 PERFORMANCE OF REED-SOLOMON FEC CODE (RS-FEC) FOR 32-PAM

Parameters	TR-DFE	MLP-DFE	
Input BER	$10^{-2}$	$10^{-2}$	$6 \times 10^{-3}$
Gross data rate	5 Gbps	10 Gbps	9.5 Gbps
RS-FEC $\{n,k\}$	$\{1023, 791\}$	$\{1023, 833\}$	$\{1023, 931\}$
Overhead [%]	22.68	18.57	8.99
Net data rate	3.87 Gbps	8.14 Gbps	8.65 Gbps
Output BER	$\approx 8.7 \times 10^{-8}$	$\approx 8.2 \times 10^{-8}$	$\approx 6.6 \times 10^{-8}$

148 no error propagation occurs; the BER results are shown in  
 149 dashed lines in Fig. 7. As expected, the effect of decision  
 150 error on both DFEs becomes more significant with increasing  
 151 data rate due to increasing ISI. The emphasis is therefore on  
 152 the highest data rate to explore the effect of decision error on  
 153 the DFEs. For 10 Gbps with 32-PAM, the BER achieved if the  
 154 transmitted symbols (i.e no error propagation) are fed back to  
 155 the conventional DFE is 0.03. This is similar to the BER when  
 156 the detected symbols are fed back to the DFE. For MLP-DFE  
 157 however, the BER is  $3 \times 10^{-3}$  when the correct symbols are fed  
 158 back. And this is superior to the BER of  $10^{-2}$  that is obtained  
 159 when the feedback inputs to the MLP-DFE are the detected  
 160 symbols. Consequently, the MLP based DFE is more prone to  
 161 error propagation than the conventional DFE. With the error  
 162 propagation effect however, FEC codes can still be used to  
 163 improve the BER performance for both DFEs as Table I shows  
 164 with the Reed-Solomon FEC code. With interleaving and more  
 165 robust FEC schemes (e.g. turbo code and low-density parity  
 166 check code), the BER performance can be further improved  
 167 with less overhead.

168 Overall, the MLP-DFE offers superior performance than the  
 169 TR-DFE for the 10 m SI-POF especially with 16-PAM and  
 170 32-PAM. This is attributed to the non-linear distortion from  
 171 the system (including the LD) that is quantified with a  $THD$   
 172 of  $-33$  dBc as Fig. 5 shows. The effect of this non-linear

173 distortion is more significant at higher PAM level. However,  
 174 MLP is more complex than the conventional equalizer as its  
 175 computational cost is of the order  $O(N_t N_{hn})$ , while that of  
 176 the conventional equalizer is  $O(N_t)$ , where  $N_t$  is the total  
 177 number of taps for the equalizer and  $N_{hn}$  is the number of  
 178 hidden layer neurons for the MLP equalizer. Furthermore,  
 179 the LMBP algorithm for MLP has the computational order  
 180 of  $O(W^2 + W N_{tr})$ , where  $W \approx N_t N_{hn}$  is the number of  
 181 synaptic weights for the MLP equalizer and  $N_{tr}$  is the number  
 182 of training examples [5]. But the RLS algorithm for TR-DFE  
 183 has the order of  $O(N_t^2)$ .

#### 184 V. CONCLUSION

185 We have successfully shown that the MLP equalizer offers  
 186 better performance than the conventional equalizers especially  
 187 for POF systems with higher order of  $M$ -PAM. With 32-PAM  
 188 and the MLP equalizer, 10 Gbps is achieved over 10 m  
 189 SI-POF at a BER of  $10^{-2}$ . To the best of the authors'  
 190 knowledge, this is the highest recorded data rate for a single  
 191 link SI-POF with PAM scheme.

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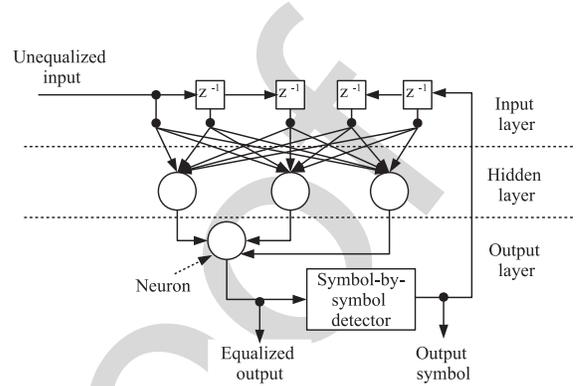


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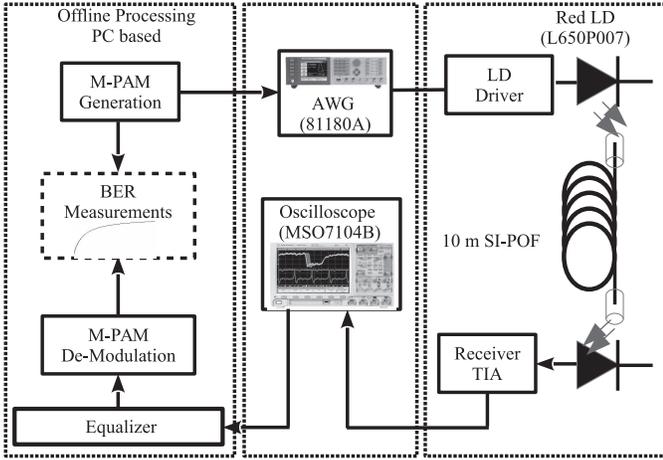


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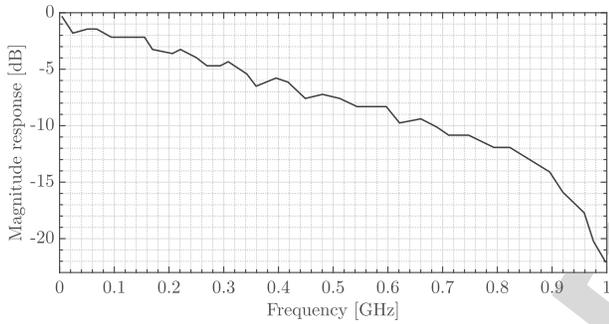


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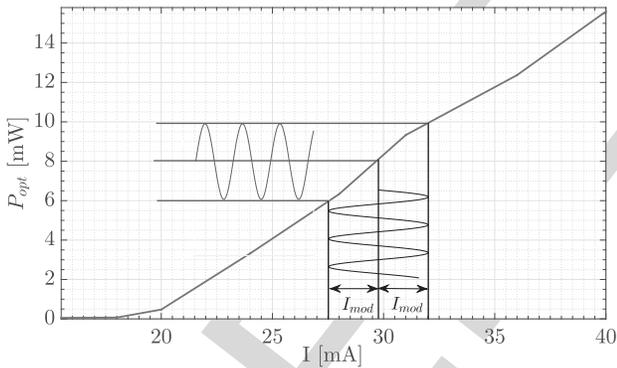


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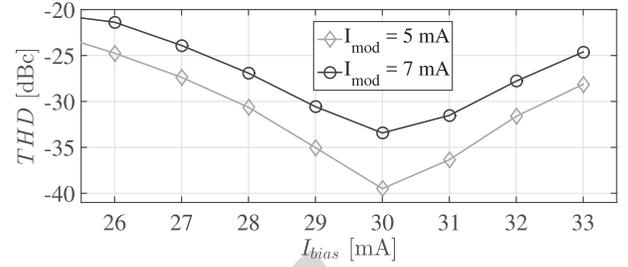


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where  $P_n$  is the power (in Watts) for the  $n^{\text{th}}$  harmonic with  $n = 1$  being the fundamental frequency. The  $THD$  is obtained by measuring the power of the first five harmonics using a fundamental frequency of 40 MHz. The plots in Fig. 5 suggest that a bias current of 30 mA should be used for minimal non-linearity from the LD. Fig. 5 particularly shows that increasing  $I_{mod}$  increases the non-linearity of the LD, as the  $THD$  is about  $-33$  dBc and  $-39$  dBc when  $I_{mod}$  is 7 mA and 5 mA respectively. The LD is therefore biased at about 30 mA with  $I_{mod} = 6.5$  mA resulting to an optical modulation index ( $\eta_{mod}$ ) of 0.65, where  $\eta_{mod} = \frac{I_{mod}}{I_{bias} - I_{th}}$ . The LD's output is then transmitted through a 10 m SI-POF (HFBR-RUD500Z, core diameter = 1 mm) with a measured attenuation of  $\sim 1.8$  dB at DC. The received optical signal from the SI-POF is converted into an electrical signal with the photo-receiver and the resulting signal is captured with an oscilloscope (MSO7104B). The captured signal is then imported into the PC for post-processing, which comprises of filtering, down-sampling and equalization. Finally, the equalized output is demodulated, thus offering the received binary data, that in turn is used to evaluate the bit-error-rate (BER).

### III. EQUALIZER PARAMETERS

The number of input taps and training examples are important parameters required for a DFE. The optimum number of taps for a DFE depends on the data rate and the PAM level. But for fair comparison under similar conditions, both DFEs have 22 forward taps and 18 feedback taps. To determine the optimum number of training examples for the DFEs, the BER is obtained for different training examples as shown in Fig. 6. The plots in Fig. 6 shows that while 2000 symbols are enough for the transversal DFE, the MLP-DFE requires at least 3000. Thus, 4000 training symbols are selected for both DFEs.

The number of hidden layer neurons is another parameter for MLP-DFE. For this study, it is chosen as six because there is no proper definition for optimum value to the best of the authors' knowledge.

modulated by mapping  $\log_2(M)$  bits to one of  $M$  amplitude levels with gray coding. The data symbols are preceded by a preamble of 4000 symbols that is used for synchronization at the receiver and for training the equalizer. The resulting symbol sequence is upsampled and fed through a digital pulse shaping filter, which for this work, is a root-raised-cosine filter with roll-off factor of 0.5. The modulated and pulse shaped signal is then loaded to an arbitrary waveform generator (AWG, Keysight 81180A).

The measured P-I curve of the LD is depicted in Fig. 4 and it shows the threshold current ( $I_{th}$ ) of the LD to be 20 mA. The non-linearity of the LD is quantified by measuring its

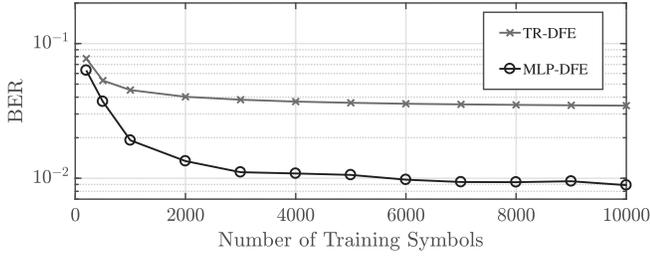


Fig. 6. BER plot comparing the equalizers' performance for different training symbols at a data rate of 10 Gbps with 32-PAM.

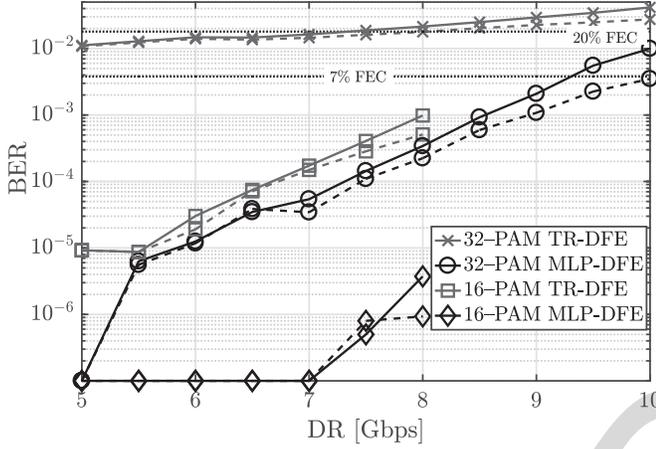


Fig. 7. BER plots comparing the MLP based DFE with the conventional DFE. The solid and dashed lines represents the BER if the feedback inputs for the DFEs are the detected and transmitted symbols respectively.

#### IV. RESULTS AND DISCUSSIONS

123 The maximum data rate at a given value of  $M$  is limited  
 124 to  $2\log_2(M)$  Gbps, because the maximum sampling rate of  
 125 the AWG is 4 Gsa/s (from Nyquist theory). So, for  $M = 4$   
 126 and  $M = 8$ , the maximum data rate is limited to 4 Gbps  
 127 and 6 Gbps respectively. Therefore, 16-PAM and 32-PAM are  
 128 considered owing to their higher spectral efficiency and the  
 129 sufficient received SNR for the 10 m SI-POF. The BER plot  
 130 for 16-PAM and 32-PAM is shown in Fig. 7. With 16-PAM,  
 131 8 Gbps is achieved at a BER of  $10^{-3}$  with the TR-DFE.  
 132 With the MLP-DFE however, the BER is  $\sim 3.5 \times 10^{-6}$   
 133 at 8 Gbps data rate. For 32-PAM, the BER at 5.5 Gbps is  
 134 about  $10^{-2}$  using the TR-DFE, but this is below  $10^{-5}$  with the  
 135 MLP equalizer. A gross data rate of 10 Gbps is achieved with  
 136 32-PAM and the MLP equalizer at a BER of  $10^{-2}$  which  
 137 is below the 20% forward error correction (FEC) limit as  
 138 Table I shows. Furthermore, the eye diagram in Fig. 8 clearly  
 139 shows a wider eye opening with the MLP-DFE compared to  
 140 the conventional DFE for 5.5 Gbps using both 16-PAM and  
 141 32-PAM.

142 A drawback of the DFE configuration is their susceptibility  
 143 to error propagation due to occasional decision error of their  
 144 input feedback symbols. To assess this error propagation effect  
 145 on the conventional DFE and MLP-DFE, the BER result is also  
 146 computed using the transmitted symbols as feedback data. This  
 147 way, the feedback signal is always error free and therefore

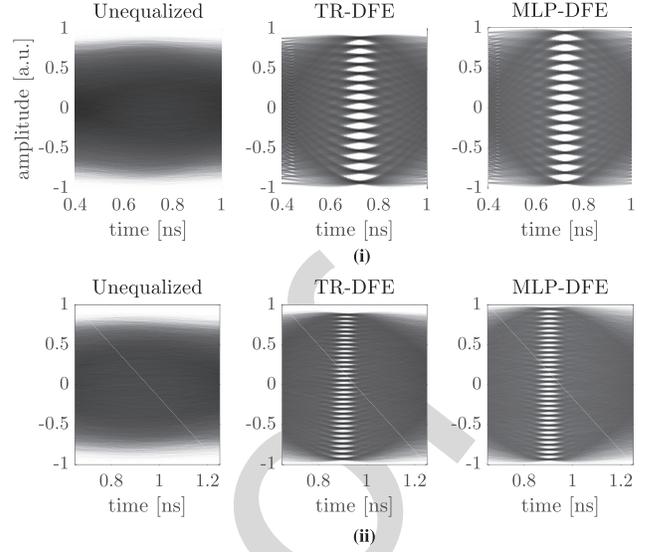


Fig. 8. Eye diagram comparing the equalizers' performance for 5.5 Gbps with: (i) 16-PAM (ii) 32-PAM.

TABLE I  
 PERFORMANCE OF REED-SOLOMON FEC CODE (RS-FEC) FOR 32-PAM

Parameters	TR-DFE	MLP-DFE	
Input BER	$10^{-2}$	$10^{-2}$	$6 \times 10^{-3}$
Gross data rate	5 Gbps	10 Gbps	9.5 Gbps
RS-FEC $\{n,k\}$	$\{1023, 791\}$	$\{1023, 833\}$	$\{1023, 931\}$
Overhead [%]	22.68	18.57	8.99
Net data rate	3.87 Gbps	8.14 Gbps	8.65 Gbps
Output BER	$\approx 8.7 \times 10^{-8}$	$\approx 8.2 \times 10^{-8}$	$\approx 6.6 \times 10^{-8}$

148 no error propagation occurs; the BER results are shown in  
 149 dashed lines in Fig. 7. As expected, the effect of decision  
 150 error on both DFEs becomes more significant with increasing  
 151 data rate due to increasing ISI. The emphasis is therefore on  
 152 the highest data rate to explore the effect of decision error on  
 153 the DFEs. For 10 Gbps with 32-PAM, the BER achieved if the  
 154 transmitted symbols (i.e no error propagation) are fed back to  
 155 the conventional DFE is 0.03. This is similar to the BER when  
 156 the detected symbols are fed back to the DFE. For MLP-DFE  
 157 however, the BER is  $3 \times 10^{-3}$  when the correct symbols are fed  
 158 back. And this is superior to the BER of  $10^{-2}$  that is obtained  
 159 when the feedback inputs to the MLP-DFE are the detected  
 160 symbols. Consequently, the MLP based DFE is more prone to  
 161 error propagation than the conventional DFE. With the error  
 162 propagation effect however, FEC codes can still be used to  
 163 improve the BER performance for both DFEs as Table I shows  
 164 with the Reed-Solomon FEC code. With interleaving and more  
 165 robust FEC schemes (e.g. turbo code and low-density parity  
 166 check code), the BER performance can be further improved  
 167 with less overhead.

168 Overall, the MLP-DFE offers superior performance than the  
 169 TR-DFE for the 10 m SI-POF especially with 16-PAM and  
 170 32-PAM. This is attributed to the non-linear distortion from  
 171 the system (including the LD) that is quantified with a  $THD$   
 172 of  $-33$  dBc as Fig. 5 shows. The effect of this non-linear

173 distortion is more significant at higher PAM level. However,  
 174 MLP is more complex than the conventional equalizer as its  
 175 computational cost is of the order  $O(N_t N_{hn})$ , while that of  
 176 the conventional equalizer is  $O(N_t)$ , where  $N_t$  is the total  
 177 number of taps for the equalizer and  $N_{hn}$  is the number of  
 178 hidden layer neurons for the MLP equalizer. Furthermore,  
 179 the LMBP algorithm for MLP has the computational order  
 180 of  $O(W^2 + W N_{tr})$ , where  $W \approx N_t N_{hn}$  is the number of  
 181 synaptic weights for the MLP equalizer and  $N_{tr}$  is the number  
 182 of training examples [5]. But the RLS algorithm for TR-DFE  
 183 has the order of  $O(N_t^2)$ .

#### 184 V. CONCLUSION

185 We have successfully shown that the MLP equalizer offers  
 186 better performance than the conventional equalizers especially  
 187 for POF systems with higher order of  $M$ -PAM. With 32-PAM  
 188 and the MLP equalizer, 10 Gbps is achieved over 10 m  
 189 SI-POF at a BER of  $10^{-2}$ . To the best of the authors'  
 190 knowledge, this is the highest recorded data rate for a single  
 191 link SI-POF with PAM scheme.

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